

Online Examinations (Even Sem/Part-I/Part-II Examinations 2020 - 2021)

Course Name - –Microprocessors

Course Code - DECE404

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Answer all the questions. Each question carry one mark.

9. 1. The memory capacity of 8085 microprocessor is

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- 64 KB
- 1 MB
- 16 MB
- 640 B

10. 2. 8085 microprocessor has how many pins

Mark only one oval.

40

39

42

20

11. 3. Which of the following microprocessors is a 4 bit microprocessor?

Mark only one oval.

4004

8080

8085

8086

12. 4. In 8085 microprocessor, name the 16 bit register is called

Mark only one oval.

Stack Pointer

PSW

Both Stack Pointer and PSW

None of these

13. 5. In a computer where microprocessor is used as CPU is known as

Mark only one oval.

- Microprocessor
- Micro-computer
- Microcontroller
- All of these

14. 6. An 8-bit microprocessor has an

Mark only one oval.

- 8 bit data bus
- 8-bit address bus
- 8-bit control bus
- 8-bit interrupt lines

15. 7. Which semiconductor technology is used for fabrication of 8085 microprocessor?

Mark only one oval.

- ECL
- NMOS
- NMOS and HMOS
- NMOS and CMOS

16. 8. If the microprocessor is capable of addressing 1MB of memory, its address bus width is

Mark only one oval.

- 16 bits
- 20 bits
- 8 bits
- none of these

17. 9. The frequency of the driving network connected between pins 1 and 2 of 8085 microprocessor is

Mark only one oval.

- twice the desired frequency
- equal to the desired frequency
- four times the desired frequency
- none of these

18. 10. Why 8085 processor is called an 8 bit processor?

Mark only one oval.

- because 8085 processor has 8 bit ALU
- because 8085 processor has 8 bit data bus
- because 8085 processor has 16 bit data bus
- because 8085 processor has 16 bit address bus

19. 11. A microprocessor is a

Mark only one oval.

- SSI device
- MSI device
- LSI device
- VLSI device

20. 12. The address bus of microprocessor is

Mark only one oval.

- unidirectional
- bi-directional
- unidirectional as well as bi-directional
- none of these

21. 13. After add 89H & 79H , the status of Parity flag will be

Mark only one oval.

- 0
- 1
- undefined
- None of these

22. 14. Which one of the following is called non-vectored interrupt?

Mark only one oval.

- TRAP
- INTR
- RST 7.5
- RST 0

23. 15. ADD C will be executed in

Mark only one oval.

- 1 Machine cycle
- 2 Machine cycle
- 3 Machine cycle
- 4 Machine cycle

24. 16. What is SIM?

Mark only one oval.

- Select Interrupt Mask
- Shoting Interrupt Mask
- Set Interrupt Mask
- None of these

25. 17. The number of T-States required for PCHL instruction execution is

Mark only one oval.

4

6

8

10

26. 18. XCHG is an

Mark only one oval.

data transfer instruction

logical instruction

arithmetic instruction

none of these

27. 19. The instruction "JUMP" belongs to

Mark only one oval.

sequential control flow instructions

control transfer instructions

branch instructions

control transfer & branch instructions

28. 20. Which instruction is required to rotate the content of accumulator one bit right along with carry?

Mark only one oval.

- RLC
 RAL
 RRC
 RAR

29. 21. What are level Triggering interrupts?

Mark only one oval.

- INTR & TRAP
 RST6.5 & RST5.5
 RST7.5 & RST6.5
 RST2.5 & RST6.2

30. 22. What are software interrupts? _____

Mark only one oval.

- RST 0-7
 RST 5.5 - 7.5
 INTR, TRAP
 RST 4.4 - 6.4

31. 23. MOV A, C is an instruction of

Mark only one oval.

- One byte
- two byte
- three byte
- four byte

32. 24. CMA is an instruction of

Mark only one oval.

- direct addressing mode
- indirect addressing mode
- register addressing mode
- Implicit addressing mode

33. 25. MVI A, 58H is an instruction of

Mark only one oval.

- direct addressing mode
- indirect addressing mode
- register addressing mode
- Immediate addressing mode

34. 26. By issuing which one of the following signal, CPU releases the system bus to DMA controller?

Mark only one oval.

- HOLD
- HLDA
- ALE
- READY

35. 27. The technique of assigning a memory address to each I/O device in the computer system is called:

Mark only one oval.

- memory-mapped I/O
- ported I/O
- dedicated I/O
- wired I/O

36. 28. In 8086 microprocessor, the size of each segment is

Mark only one oval.

- 32 KB
- 64 KB
- 16 KB
- 8 KB

37. 29. In which year, 8086 microprocessor was introduced?

Mark only one oval.

1978

1979

1977

1971

38. 30. In 8086 microprocessor the following has the highest priority among all type interrupts?

Mark only one oval.

NMI

DIV 0

TYPE 255

OVER FLOW

39. 31. 8088 microprocessor has

Mark only one oval.

16 bit data bus

4 byte pre-fetch queue

6 byte pre-fetch queue

16 bit address bus

40. 32. What is addressing mode of instruction MOV AX, [BX]?

Mark only one oval.

- register addressing mode
- immediate addressing mode
- register indirect addressing mode
- indirect addressing mode

41. 33. Which of the following is based with 16 bit displacement addressing?

Mark only one oval.

- MOV AX, [BX+06]
- MOV AX, [BP+2000]
- MOV AX, [BP+06]
- MOV AX, [BP]

42. 34. Instruction queue of 8086 consist of

Mark only one oval.

- 6 data
- 8 data
- 4 data
- 10 data

43. 35. Segment memory capacity of 8086 is

Mark only one oval.

1 MB

64 KB

2 MB

4 MB

44. 36. Which of the following instruction is a logical instruction?

Mark only one oval.

DIV AB

TEST

CALL

AAM

45. 37. DS Stand for

Mark only one oval.

Data segment

Direct segment

Declare segment

Divide segment

46. 38. How many register banks are present in 8051 microcontroller?

Mark only one oval.

4

3

2

1

47. 39. Which of the following comes under the indexed addressing mode?

Mark only one oval.

MOVX A, @DPTR

MOVC @A+DPTR,A

MOV A,R0

MOV @R0,A

48. 40. Which of the following registers are not bit addressable?

Mark only one oval.

SCON

PCON

A

PSW

49. 41. When 8051 wakes up then 0x00 is loaded to which register?

Mark only one oval.

DPTR

SP

PC

PSW

50. 42. Which of the following register can be used to hold address of byte in the memory of 8051?

Mark only one oval.

DPTR

PCON

SBUF

PSW

51. 43. The 8051 microcontroller is of ___pin package as a _____ processor.

Mark only one oval.

30, 1byte

20, 1 byte

40, 8 bit

40, 8 byte

52. 44. JZ, JNZ, DJNZ, JC, JNC instructions monitor the bits of which register in 8051?

Mark only one oval.

DPTR

B

A

PSW

53. 45. Which of the following instruction is incorrect?

Mark only one oval.

CPL A

SWAP A

CLR C

RL B

54. 46. Consider the following registers: a. Accumulator and flag register b. B and C register c. D and E register d. H and L register Which of these 8-bit registers of 8085 microprocessor can be paired together to make a 16-bit register?

Mark only one oval.

a,c,d

b,c,d

a,b,d

a,b,c

55. 47. Which stack is used in 8085?

Mark only one oval.

- FIFO
- LIFO
- FILO
- LILO

56. 48. The cycle required to fetch and execute an instruction in a 8085 microprocessor is which one of the following?

Mark only one oval.

- Clock cycle
- Memory cycle
- Machine cycle
- Instruction cycle

57. 49. A sequence of two registers that multiplies the content of DE register pair by two and stores the result in HL register pair (in 8085 assembly language) is

Mark only one oval.

- XCHG & DAD B
- XTHL & DAD H
- PCHL & DAD D
- XCHG & DAD H

58. 50. MOV A,C is executed by

Mark only one oval.

- one machine cycle
- two machine cycle
- three machine cycle
- four machine cycle

59. 51. IN 00H is an instruction of

Mark only one oval.

- direct addressing mode
- indirect addressing mode
- register addressing mode
- Immediate addressing mode

60. 52. The 16 bit flag of 8086 microprocessor is responsible to indicate

Mark only one oval.

- the condition of result of ALU operation
- the condition of memory
- the result of addition
- the result of subtraction

61. 53. OUT 00H is an instruction of

Mark only one oval.

- direct addressing mode
- indirect addressing mode
- register addressing mode
- Immediate addressing mode

62. 54. LHLD 2500H is an instruction of

Mark only one oval.

- direct addressing mode
- indirect addressing mode
- register addressing mode
- Immediate addressing mode

63. 55. ADI, 01 H is an instruction of

Mark only one oval.

- direct addressing mode
- indirect addressing mode
- register addressing mode
- Immediate addressing mode

64. 56. CMP B is an instruction of

Mark only one oval.

- direct addressing mode
- indirect addressing mode
- register addressing mode
- Immediate addressing mode

65. 57. How many T-states are required for execution of LHLD 3600 H instruction?

Mark only one oval.

- 10
- 13
- 16
- 7

66. 58. How many T-states are required for execution of MVI A, 05H instruction?

Mark only one oval.

- 10
- 4
- 7
- 13

67. 59. How many T-states are required for execution of JNZ 2602H instruction?

Mark only one oval.

16

13

10

20

68. 60. LXI H, 2500 H is executed by

Mark only one oval.

five machine cycle

two machine cycle

three machine cycle

four machine cycle

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