

Online Examinations (Even Sem/Part-I/Part-II Examinations 2020 - 2021)

Course Name - –Microprocessor & Microcontroller

Course Code - EC401

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Answer all the questions. Each question carry one mark.

9. 1. The word size of 8085 microprocessor is

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- 16-bits
- 20-bits
- 8-bits
- 4-bits

10. 2. Both the ALU and control section of CPU employ which special purpose storage location?

Mark only one oval.

- Buffers
- Decoders
- Accumulators
- Registers

11. 3. Which stack is used in 8085?

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- FIFO
- FILO
- LIFO
- LILO

12. 4. Name of typical dedicated register is:

Mark only one oval.

- PC
- IR
- All of these
- SP

13. 5. The program counter in microprocessor

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- counts the numbers of instructions being executed on the microprocessor
- counts the number of program being executed on the microprocessor
- keeps the address of the next instruction to be fetched
- counts the number of interrupts handled by the microprocessor

14. 6. The address bus of microprocessor is

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- bi-directional
- unidirectional as well as bi-directional
- unidirectional
- none of these

15. 7. A microprocessor performs as

Mark only one oval.

- memory of a computer
- output device of a computer
- CPU of a computer
- input device of a computer

16. 8. READY signal in 8085 is useful when the CPU communicates with

Mark only one oval.

- a fast peripheral device
- a DMA chip
- a slow peripheral device
- a PPI

17. 9. The 64 bit processor is

Mark only one oval.

- Pentium
- Pentium II
- Pentium 4
- Pentium III

18. 10. Consider the following I) Sign flag II) Trap flag III) Parity flag IV) Auxiliary carry flag Which one of the above flags is/are present in 8085 microprocessor?

Mark only one oval.

- (I) only
- (I) & (II)
- (I) ,(III) & (IV)
- (II) & (III)

19. 11. After add 89H & 79H , the status of Parity flag will be

Mark only one oval.

- 0
- x
- 1
- Unchanged

20. 12. What is SIM?

Mark only one oval.

- Select Interrupt Mask
- Shoting Interrupt Mask
- Set Interrupt Mask
- None of these

21. 13. HLT op-code means

Mark only one oval.

- Load data to accumulator
- Store result in memory
- Load accumulator with contents of register
- End of program

22. 14. The instruction format 'register to register' has a length of

Mark only one oval.

- 2 bytes
- 3 bytes
- 1 byte
- 4 bytes

23. 15. Which instruction is required to rotate the content of accumulator one bit right along with carry?

Mark only one oval.

- RLC
- RAL
- RAR
- RRC

24. 16. LXI H, 2500 H is an instruction of

Mark only one oval.

- direct addressing mode
- indirect addressing mode
- Immediate addressing mode
- register addressing mode

25. 17. XCHG is an

Mark only one oval.

- arithmetic instruction
- logical instruction
- data transfer instruction
- I/O and stack pointer instruction

26. 18. A two byte instruction consists of

Mark only one oval.

- opcode only
- opcode and two operands
- opcode and operand
- operand only

27. 19. How many times will the following loop be executed? XRA A MVI C, 05H LOOP; DCRC JNZ LOOP

Mark only one oval.

- once
- infinite times
- five times
- depends on initial value of A

28. 20. The number of stop bits in case of asynchronous data transfer is

Mark only one oval.

4 bits

3 bits

2 bits

5 bits

29. 21. How many address lines are required to access 1 MB RAM using microprocessor?

Mark only one oval.

16

8

20

12

30. 22. Which flags of 8086 are not present in 8085?

Mark only one oval.

TF

DF

All of these

OF

31. 23. What is the size of instruction Queue of 8086 microprocessor?

Mark only one oval.

3

5

6

8

32. 24. In 8086 microprocessor, the size of each segment is

Mark only one oval.

32 KB

16 KB

64 KB

8 KB

33. 25. The Intel 8086 microprocessor is a _____ processor.

Mark only one oval.

8 bit

32 bit

16 bit

4 bit

34. 26. In 8086 the overflow flag is set when _____.

Mark only one oval.

- the sum is more than 16 bits
- carry and sign flags are set
- signed numbers go out of their range after an arithmetic operation
- subtraction

35. 27. Which of the following is based with 16 bit displacement addressing?

Mark only one oval.

- MOV AX, [BX+06]
- MOV AX, [BP+06]
- MOV AX, [BP+2000]
- MOV AX, [BP]

36. 28. 2's compliment instruction is

Mark only one oval.

- NOT
- CMP
- NEG
- CMC

37. 29. Instruction queue of 8088 consist of

Mark only one oval.

- 6 data
- 8 data
- 4 data
- 10 data

38. 30. Memory map of 8086 is

Mark only one oval.

- 0000H to FFFFH
- 0000H to 9FFFH
- 00000H to FFFFFH
- none of these

39. 31. How many register banks are present in 8051 microcontroller?

Mark only one oval.

- 3
- 2
- 4
- 1

40. 32. In 8085 microprocessor, for execution of the instruction DCX Rp (Rp is register pair) how many T-states are required?

Mark only one oval.

- 4
- 3
- 6
- 1

41. 33. The address bus of 8085 microprocessor is

Mark only one oval.

- 20 bit
- 8 bit
- 16 bit
- 24 bit

42. 34. ALU (Arithmetic and Logic Unit) of 8085 microprocessor consists of

Mark only one oval.

- Accumulator, temporary register, arithmetic and logic circuits
- Accumulator, arithmetic, logic circuits and five flags
- Accumulator, temporary register, arithmetic, logic circuits and five flags
- Accumulator, arithmetic and logic circuits

43. 35. Which of the following microprocessors is a 4 bit microprocessor?

Mark only one oval.

8080

8085

4004

8086

44. 36. Which pin is used for demultiplexing of address and data bus of 8085 microprocessor?

Mark only one oval.

TRAP

IO/M'

ALE

READY

45. 37. Which pin is used for demultiplexing of address and data bus of 8085 microprocessor?

Mark only one oval.

TRAP

READY

ALE

END

46. 38. The program counter in a 8085 micro-processor is a 16-bit register, because

Mark only one oval.

- There are 16 address lines
- It facilitates the user storing 16-bit data temporarily
- It counts 16-bits at a time
- It has to fetch two 8-bit data at a time

47. 39. Consider the following statements: In 8085 microprocessor, data-bus and address bus are multiplexed in order to I)Increase the speed of microprocessor. II)Reduce the number of pins. III)Connect more peripheral chips. Which of these statements is/are correct?

Mark only one oval.

- (I) only
- (II) & (III)
- (I) Only
- (I) ,(III) & (IV)

48. 40. If the microprocessor is capable of addressing 64 Kbytes of memory, its address bus width is

Mark only one oval.

- 20 bits
- 8 bits
- 16 bits
- none of these

49. 41. Temporary registers in 8085 are

Mark only one oval.

B and C

D and E

W and Z

H and L

50. 42. Why 8085 processor is called an 8 bit processor?

Mark only one oval.

because 8085 processor has 8 bit ALU

because 8085 processor has 16 bit data bus

because 8085 processor has 8 bit data bus

because 8085 processor has 16 bit address bus

51. 43. A microprocessor is a

Mark only one oval.

SSI device

MSI device

LSI device

VLSI device

52. 44. The data bus of microprocessor is

Mark only one oval.

- unidirectional
- unidirectional as well as bi-directional
- bi-directional
- none of these

53. 45. The parity flag of 8085 microprocessor is set to HIGH when

Mark only one oval.

- The total number of 1's is odd
- The total number of 1's is odd and the total number of 1's is even
- The total number of 1's is even
- None of these

54. 46. Which one of the following is non-maskable interrupt of 8085 microprocessor?

Mark only one oval.

- RST 5.5
- RST 6.5
- TRAP
- RST 5

55. 47. ADD C will be executed in

Mark only one oval.

- 2 Machine cycle
- 3 Machine cycle
- 1 Machine cycle
- 4 Machine cycle

56. 48. The CALL location for TRAP is

Mark only one oval.

- 0023H
- 0042H
- 0024H
- 0032H

57. 49. When CALL instruction is executed, the SP will be

Mark only one oval.

- SP-1
- SP+1
- SP-2
- SP+2

58. 50. In 8085 microprocessor, the RST6 instruction transfer Programme execution to following location

Mark only one oval.

- 0032H.
 0048H.
 0030H.
 0024H.

59. 51. In an Intel 8085A, which is the first machine cycle of an instruction?

Mark only one oval.

- A memory write cycle
 A memory read cycle
 An op-code fetch cycle
 An I/O read cycle

60. 52. Which of the following instruction is not possible in 8085?

Mark only one oval.

- POP PSW
 POP B
 POP 30 H
 POP D

61. 53. Which interrupt has the highest priority?

Mark only one oval.

- INTR
- RST6.5
- TRAP
- RST6.6

62. 54. What are level Triggering interrupts?

Mark only one oval.

- INTR & TRAP
- RST7.5 & RST6.5
- RST6.5 & RST5.5
- RST2.5 & RST6.2

63. 55. Which interrupt is not level sensitive in 8085?

Mark only one oval.

- RST 6.5
- RST 5.5
- RST 7.5
- RST 4.5

64. 56. MOV A, C is an instruction of

Mark only one oval.

- two byte
- three byte
- One byte
- four byte

65. 57. CMA is an instruction of

Mark only one oval.

- direct addressing mode
- indirect addressing mode
- Implicit addressing mode
- register addressing mode

66. 58. MVI A, 58H is an instruction of

Mark only one oval.

- direct addressing mode
- indirect addressing mode
- Immediate addressing mode
- register addressing mode

67. 59. During IO operation the IO/M['] signal will be

Mark only one oval.

- LOW
- Tristate
- HIGH
- None of these

68. 60. By issuing which one of the following signal, CPU releases the system bus to DMA controller?

Mark only one oval.

- HOHLDALD
- ALE
- HOLD
- READY

69. 61. The technique of assigning a memory address to each I/O device in the computer system is called:

Mark only one oval.

- wired I/O
- ported I/O
- memory-mapped I/O
- dedicated I/O

70. 62. 8086 has

Mark only one oval.

- 8 bit data bus and 20 bit address bus
- 16 bit data bus and 16 bit address bus
- 16 bit data bus and 20 bit address bus
- 8 bit data bus and 16 bit address bus

71. 63. The register AX is formed by grouping _____.

Mark only one oval.

- BH & BL
- CH & CL
- AH & AL
- DH & DL

72. 64. The instruction, MOV AX, 1234H is an example of

Mark only one oval.

- register addressing mode
- based indexed addressing mode
- immediate addressing mode
- direct addressing mode

73. 65. 16 bit register of 8086 consist of

Mark only one oval.

16 flags

8 flags

9 flags

7 flags

74. 66. Instruction queue of 8086 consist of

Mark only one oval.

8 data

4 data

6 data

10 data

75. 67. Physical address of 8086 is

Mark only one oval.

8 bits

16 bits

20 bits

32 bits

76. 68. DS Stand for

Mark only one oval.

- Direct segment
- Declare segment
- Data segment
- Divide segment

77. 69. The Microcontroller 8051 is

Mark only one oval.

- 16-bit processor
- 32-bit processor
- 8-bit processor
- 64-bit processor

78. 70. AT89C2051 has RAM of:

Mark only one oval.

- 256 bytes
- 64 bytes
- 128 bytes
- 512 bytes

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