Online Examinations (Even Sem/Part-I/Part-II Examinations 2020 - 2021

Course Name - - Microcontrollers Course Code - PCC-EC403

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Answer all the questions. Each question carry one mark.

9. 1.Consider the following registers: 1. Accumulator and flag register 2. B and C register 3. D and E register 4. H and L register Which of these 8-bit registers of 8085 microprocessor can be paired together to make a 16-bit register?

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1 ,3 and 4
2 ,3 and 4
1 ,2 and 4
1 ,2 and 3

10. 2. The address bus of 8085 microprocessor is

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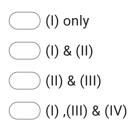
16 bit
20 bit
8 bit
24 bit

11. 3. Which of the following microprocessors is a 4 bit microprocessor?

- 4004
 8080
- 8085
- ____ Z80
- 12. 4. The program counter in a 8085 micro-processor is a 16-bit register, because *Mark only one oval.*
 - 🔵 It counts 16-bits at a time
 - There are 16 address lines
 - It facilitates the user storing 16-bit data temporarily
 - 📃 It has to fetch two 8-bit data at a time

13. 5.Consider the following I) Sign flag II) Trap flag III) Parity flag IV) Auxiliary carry flag Which one of the above flags is/are present in 8085 microprocessor?

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14. 6.If the microprocessor is capable of addressing 1MB of memory, its address bus width is

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\bigcirc	16 bits
\bigcirc	20 bits
\bigcirc	8 bits
\bigcirc	none of these

15. 7.Both the ALU and control section of CPU employ which special purpose storage location?

- Buffers
- Decoders
- Accumulators
- Registers

16. 8.Accumulator based microprocessor example are:

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Intel 8085

- Motorola 6809
- Intel 8085 and Motorola 6809
- None of these
- 17. 9.The data bus of microprocessor is

Mark only one oval.

- Unidirectional
- bi-directional
- _____ unidirectional as well as bi-directional
- none of these
- 18. 10.What is the meaning of the instruction MVI A,05H?

- data 05H is stored in the accumulator
- fifth bit of accumulator is set to one
- address 05H is stored in the accumulator
- none of the mentioned

19. 11. ADD C will be executed in

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1 Machine cycle

- 2 Machine cycle
- 3 Machine cycle
- 4 Machine cycle

20. 12.When CALL instruction is executed, the SP will be

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- SP-1
- **SP-2**
- SP+2
- 21. 13.What is meant by maskable interrupts?

- an interrupt which can never be turned off
- ____ an interrupt that can be turned off by the programmer
- an interrupt which can never be turned on
- an interrupt which can never be turned on or off

22. 14. Which interrupt has the highest priority?

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23. 15. Which interrupt is not level sensitive in 8085?

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- RST 6.5 RST 7.5 RST 5.5 RST 4.5
- 24. 16. By issuing which one of the following signal, CPU releases the system bus to DMA controller?

- HOLD
- HLDA
- READY

25. 17. The technique of assigning a memory address to each I/O device in the computer system is called:

Mark only one oval.

- memory-mapped I/O
- ported I/O
- dedicated I/O
- wired I/O
- 26. 18.What is the size of instruction Queue of 8086 microprocessor?

Mark only one oval.

- 3 5 6 8
- 27. 19. In 8086 microprocessor one of the following statements is not true?

- ____ coprocessor is interfaced in max mode
- coprocessor is interfaced in min mode
- i/o can be interfaced in max / min mode
- supports pipelining

28. 20.Which of the following is a four byte instruction?

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- MOV AX, 2345
- MUL BX
- 🔵 DIV CL
- ADD AX, [BP+0200]

29. 21. 8086 has

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- 6 memory segment
- 8 memory segment
- 4 memory segment
- 10 memory segment
- 30. 22. Which of the following instruction is a logical instruction?

Mark only one oval.

- _____ TEST

31. 23. DS Stand for

Mark only one oval.

Data segment

- Direct segment
- Declare segment
- Divide segment
- 32. 24. Which of the following registers are not bit addressable?

Mark only one oval.

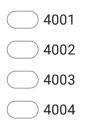


33. 25.LCALL instruction takes

- 2 bytes
- ____ 4 bytes
- 3 bytes
- 🔵 1 byte

34. 26.The first microprocessor was

Mark only one oval.



35. 27. The microprocessor was introduced in the year

Mark only one oval.

- 1940
 1971
 1973
 1980
- 36. 28. The number of output pins in 8085 microprocessors are

Mark only one oval.



28

37. 29.In 8085 microprocessor, name the 16 bit register is called

Mark only one oval.

Stack PointerStack Pointer

🔵 PSW

- Both Stack Pointer and PSW
- None of these
- 38. 30.Intel 8085A microprocessor ALE signal is made high to

Mark only one oval.

- Enable the data bus to be used as low order address bus
- To latch data D0-D7 from data bus
- 🔵 To disable data bus
- To achieve all the functions listed above
- 39. 31.Consider the following statements: In 8085 microprocessor, data-bus and address bus are multiplexed in order to I)Increase the speed of microprocessor.
 II)Reduce the number of pins. III)Connect more peripheral chips. Which of these statements is/are correct?

Mark only one oval.

(I) only
 (I) Only
 (II) & (III)
 (I) , (III) & (IV)

40. 32. Which of the following statements for Intel 8085 is correct?

Mark only one oval.

- Program Counter (PC) specifies the address of the instruction last executed
- PC specifies the address of the instruction being executed
- PC specifies the address of the instruction to be executed
- PC specifies the number of instructions executed so far
- 41. 33.The number of flags of 8085 microprocessor is

Mark only one oval.

- 6
- 5
- 3
- 42. 34. READY signal in 8085 is useful when the CPU communicates with

- a slow peripheral device
- a fast peripheral device
- 🔵 a DMA chip
- 🔵 a PPI

43. 35. The word size of 8085 microprocessor is

Mark only one oval.

8-bits 16-bits 20-bits 4-bits

44. 36.How many times will the following loop be executed? XRA A MVI C, 05H LOOP; DCRC JNZ LOOP

Mark only one oval.

\subset	once
\subset	five
() infinite times

four times

45. 37. The address bus of microprocessor is

Mark only one oval.

unidirectional

bi-directional

_____ unidirectional as well as bi-directional

none of these

46. 38.After add 89H & 79H , the status of Parity flag will be

Mark only one oval.

0
<u> </u>
oundefined
none of these

47. 39. What does microprocessor speed depends on?

Mark only one oval.

(Clock
	01001

- Data bus width
- Address bus width
- Control bus width
- 48. 40.In 8085 microprocessor, for execution of the instruction DCX Rp (Rp is register pair) how many T-states are required?



- 3
- 1

49. 41. The CALL location for TRAP is

Mark only one oval.

0023H
 0032H
 0042H

- 0024H
- 50. 42.The number of T-States required for PCHL instruction execution is

Mark only one oval.

- 4
 6
 8
 10
- 51. 43. HLT op-code means

- Load data to accumulator.
- Store result in memory.
 - Duad accumulator with contents of register.
- End of program

52. 44.In an Intel 8085A, which is the first machine cycle of an instruction?

Mark only one oval.

An op-code fetch cycle

A memory write cycle

- A memory read cycle
- An I/O read cycle
- 53. 45. How many T-states are required for execution of OUT 80H instruction?

Mark only one oval.

- 10
 13
 16
 8
- 54. 46.What is the RST for the TRAP?

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RST5.5
 RST4.5
 RST4
 RST4

55. 47.STA 2070H is an instruction of

Mark only one oval.

One byte

- two byte
- three byte
- _____ four byte

56. 48. MOV A, B is an instruction of

Mark only one oval.

direct addressing mode

indirect addressing mode

register addressing mode

Immediate addressing mode

57. 49.CMA is an instruction of

Mark only one oval.

- direct addressing mode
- indirect addressing mode



Implicit addressing mode

58. 50.During IO operation the IO/M ' signal will be

Mark only one oval.

LOW
Tristate
HIGH
O None of these

59. 51.In 8086 microprocessor, the size of each segment is

Mark only one oval.



60. 52. The accumulator is 16 bit wide of 8086 and is called



- DL

61. 53.In which year, 8086 microprocessor was introduced?

Mark only one oval.



62. 54. In 8086 the overflow flag is set when _____.

Mark only one oval.

- the sum is more than 16 bits
- signed numbers go out of their range after an arithmetic operation
- carry and sign flags are set
- subtraction
- 63. 55.The instruction, MOV AX, 1234H is an example of

- register addressing mode
- immediate addressing mode
- based indexed addressing mode
- direct addressing mode

64. 56. Which of the following instruction is immediate addressing?

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- ____ MOV AX, [2000]
- ONV BX, 2000
- MOV AX, [SI]
- MOV AX, BX
- 65. 57.2's compliment instruction is

Mark only one oval.

- **NEG**
- CMP
- CMC
- 66. 58. Instruction queue of 8088 consist of

- 6 data
- 🔵 4 data
- 🔵 10 data

67. 59. Memory map of 8086 is

Mark only one oval.

0000H to FFFFH

00000H to FFFFFH

0000H to 9FFFH

none of these

68. 60.Physical address of 8086 is

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- 8 bits
- 16 bits
- 20 bits
- 32 bits

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