

Online Assessment (Even Sem/Part-I/Part-II Examinations 2019 - 2020)

Course Name - Computer Architecture and Organization

Course Code - BCA203(BL)

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Answer all the questions. Each question carry one mark.

9. 1. Between the instruction ADD D and the instruction SUB D, there is

Mark only one oval.

- Dependence
- Anti-dependence
- Correlation
- Scheduling

10. 2. Allowing multiple instructions for issuing in a clock cycle, is the goal of

Mark only one oval.

- Single-issue processors
- Dual-issue processors
- Multiple-issue processors
- No-issue processors

11. 3.If event occurs at same place every time program is executed with same data and memory allocation, then event is known as

Mark only one oval.

- Asynchronous
- Synchronous
- Delayed
- Stalled

12. 4. The control unit controls other units by generating _____

Mark only one oval.

- Control signals
- Timing signals
- Transfer signals
- Command Signals

13. 5. Pipelining increases CPU instruction, by-

Mark only one oval.

- Size
- Throughput
- Time
- Cycle rate

14. 6. In the case of, Zero-address instruction method the operands are stored in ____

Mark only one oval.

- Registers
- Accumulators
- Push down stack
- Cache

15. 7. Sum of contents in a base register and sign-extended offset is used as a memory address, sum is known as

Mark only one oval.

- ALU instructions
- Throughput
- Effective address
- Load and store instructions

16. 8. The number successful accesses to memory stated as a fraction is called as ____

Mark only one oval.

- Hit rate
- Miss rate
- Success rate
- Access rate

17. 9. Situations that prevent next instruction in instruction stream, from executing during its designated clock cycle are known as _____

Mark only one oval.

- Pipe stage
- Previous stage
- Hazards
- Processor cycle

18. 10. What is the binary equivalent of the decimal number 368?

Mark only one oval.

- 101110000
- 110110000
- 111010000
- 111100000

19. 11. 1's complement representation of decimal number of -17 by using 8 bit representation is

Mark only one oval.

- 1110 1110
- 1101 1101
- 1100 1100
- 0001 0001

20. 12. In a positive logic system, logic state 1 corresponds to

Mark only one oval.

- positive voltage
- higher voltage level
- zero voltage level
- lower voltage level

21. 13. The 2's complement of the number 1101110 is

Mark only one oval.

- 0010001
- 0110001
- 0010010
- None of these

22. 14. The method of synchronizing the processor with the I/O device in which the device sends a signal when it is ready is

Mark only one oval.

- Exceptions
- Signal handling
- Interrupts
- DMA

23. 15. The number successful accesses to memory stated as a fraction is called as

Mark only one oval.

- Hit rate
- Miss rate
- Success rate
- Access rate

24. 16. Processor without structural hazard is

Mark only one oval.

- Faster
- Slower
- Have longer clock cycle
- have larger clock rate

25. 17. The advantage of I/O mapped devices to memory mapped is

Mark only one oval.

- The former offers faster transfer of
- The devices connected using I/O
- The devices have to deal with fewer address lines
- None of these

26. 18. To read the control words sequentially _____ is used.

Mark only one oval.

- PC
- IR
- MPC(Micro program counter)
- None of these

27. 19. Physical memory broken down into groups of equal size is called

Mark only one oval.

- Page
- block/frame
- tag
- None of these

28. 20. Which sign bit is used for representing the positive sign in floating point representation?

Mark only one oval.

- 1
- 0
- either 1 or 0
- None of these

29. 21. During a write operation if the required block is not present in the cache then _____ occurs

Mark only one oval.

- Write latency
- Write hit
- Write delay
- Write miss

30. 22. The memory blocks are mapped on to the cache with the help of __

Mark only one oval.

- Hash functions
- Vectors
- Mapping functions
- None of these

31. 23. A sequence of control words corresponding to a control sequence is called

Mark only one oval.

- Micro routine
- Micro function
- Micro procedure
- None of these

32. 24. Execution of several activities at the same time is called

Mark only one oval.

- processing
- parallel processing
- serial processing
- multitasking

33. 25. A pipeline is like-

Mark only one oval.

- an automobile assembly line
- house pipeline
- Bothan automobile assembly line & house pipeline
- a gas line

34. 26. The ability to temporarily halt the CPU and use this time to send information on buses is called

Mark only one oval.

- Direct memory access
- Vectoring the interrupt
- System Interrupt
- Cycle stealing

35. 27. A word whose individual bits represent a control signal is _____

Mark only one oval.

- Command word
- Control word
- Coordination word
- Generation word

36. 28. The unit of a computer system which executes program, communicates with and often controls the operation of other subsystems of the computer is the

Mark only one oval.

- CPU
- Control unit
- both CPU & Control unit
- Peripheral unit

37. 29. In micro-programmed approach, the signals are generated by _____

Mark only one oval.

- Machine instructions
- System program
- Utility tools
- None of these

38. 30. In a computer, ALU can perform

Mark only one oval.

- addition
- subtraction
- multiplication
- all of these

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