

Online Assessment (Even Sem/Part-I/Part-II Examinations 2019 - 2020)

Course Name - Computer Organization and Architecture

Course Code - BCAC202

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Answer all the questions. Each question carry one mark.

9. 1. In a computer, ALU can perform

Mark only one oval.

- addition
- subtraction
- multiplication
- all of these

10. 2. In the case of, Zero-address instruction method the operands are stored in _____

Mark only one oval.

- Registers
- Money
- Push down stack
- Cache

11. 3. 1's complement representation of decimal number of -18 by using 8 bit representation is

Mark only one oval.

- 1110 1101
- 1101 1101
- 1100 1100
- 0001 0001

12. 4. The transfer of large chunks of data with the involvement of the processor is done by

Mark only one oval.

- DMA controller
- Arbitrator
- User system programs
- None of these

13. 5. During a write operation if the required block is not present in the cache then _____ occurs.

Mark only one oval.

- Write latency
- Write hit
- Write delay
- Write miss

14. 6. The final addition sum of the numbers, 0111 & 0110 is _____

Mark only one oval.

1101

1111

1001

1010

15. 7. Which representation is most efficient to perform arithmetic operations on the numbers?

Mark only one oval.

Sign-magnitude

1's complement

2'S complement

None of these

16. 8. Pipelining increases CPU instruction, by-

Mark only one oval.

Size

Throughput

Cycle rate

Time

17. 9. The addressing mode used in the instruction MOV A,B is

Mark only one oval.

- Direct
- Register
- Register indirect
- Index

18. 10. The principle of locality justifies the use of

Mark only one oval.

- Interrupt
- DMA
- Polling
- Cache memory

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