

Online Assessment (Even Sem/Part-I/Part-II Examinations 2019 - 2020)

Course Name - --Microprocessors

Course Code - DCSE404

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Answer all the questions. Each question carry one mark.

9. 1. Which of the following is the most complex ?

Mark only one oval.

- Microcontroller
- Microprocessor
- Microcomputer
- None of these

10. 2. Which of the following instruction will perform "Do Nothing"

Mark only one oval.

- NOP
- MOV
- HLT
- None of these

11. 3. An 16K*8 ROM, holding the monitor program in a microprocessor trainer kit has end address?

Mark only one oval.

- 1FFF H
- 2FFF H
- 3FFF H
- 4FFF H

12. 4. In case of subroutine calling, which of the following registers used

Mark only one oval.

- Stack pointer
- Program counter
- Both Stack pointer and Program counter
- None of these

13. 5. Who is the vendor of 8085 Microprocessor?

Mark only one oval.

- Motorola
- Intel
- IBM
- TI

14. 6. IC 8259 is a

Mark only one oval.

- Programmable DMA Controller
- Programmable Interrupt Controller
- Programmable Peripheral Controller
- Programmable Keyboard Controller

15. 7. At the time of ongoing DMA operation, 8085 belongs to

Mark only one oval.

- Master Mode
- Slave Mode
- Neutral Mode
- Any one of the above

16. 8. In IC 8259, Cascade lines are used to

Mark only one oval.

- support multiple interrupts at a time
- support multiple 8259 IC at a time
- support multiple microprocessor
- None of these

17. 9. Name of the program responsible to transfer hex input to binary is

Mark only one oval.

- System Program
- Application Program
- Compiler
- Monitor Program

18. 10. The only unconditional branch instruction in 8085 is

Mark only one oval.

- JMP
- JNC
- JNZ
- JZ

19. 11. In a computer where microprocessor is used as CPU is known as

Mark only one oval.

- Microprocessor
- Microcontroller
- Micro-computer
- All of these

20. 12. To de-multiplex address-data bus, which pin is used?

Mark only one oval.

HOLD

HLDA

INTR

ALE

21. 13. In which of the operation types, no flag is affected?

Mark only one oval.

Data transfer

Arithmetic

Machine Control

Branching

22. 14. Which is not a valid register pair?

Mark only one oval.

BC

DE

BL

HL

23. 15. Addressing mode of HLT instruction is

Mark only one oval.

- Immediate
- Implied
- Stack
- Direct

24. 16. Which flag is used for BCD Addition?

Mark only one oval.

- Carry
- Parity
- Auxiliary Carry
- Sign

25. 17. MVI B, 32 H is a

Mark only one oval.

- 1 Byte instruction
- 2 Byte instruction
- 3 Byte instruction
- Not supported by 8085 microprocessor

26. 18. In Memory Read machine cycle, numbers of T-states required is

Mark only one oval.

3

5

4

6

27. 19. LDA 8050H requires ____ numbers of machine cycle

Mark only one oval.

3

4

1

None of these

28. 20. How many T-States are there in STA 8050H instruction?

Mark only one oval.

9

10

11

13

29. 21. Which one requires 4 t-States to complete?

Mark only one oval.

- Memory Read
- I/O Write
- Op-code Fetch
- Memory Write

30. 22. MVI A, 32H requires how many machine cycles?

Mark only one oval.

- 1
- 2
- 3
- 4

31. 23. MOV A, B requires how many machine cycles?

Mark only one oval.

- 2
- 3
- 4
- 1

32. 24. Which combination of S1 and S0 is for Halt?

Mark only one oval.

01

10

00

11

33. 25. Which stack is used in 8085?

Mark only one oval.

LIFO

FILO

FIFO

None of these

34. 26. What is SIM?

Mark only one oval.

Select Interrupt Mask

Sorting Interrupt Mask

Set Interrupt Mask

None of these

35. 27. What is meant by Maskable interrupts?

Mark only one oval.

- An Interrupt that can be turned off by the programmer
- Both An Interrupt which can never be Turned off and An Interrupt that can be turned off by the programmer
- An Interrupt which can never be Turned off
- None of these

36. 28. Address line for RST3 is?

Mark only one oval.

- 0020H
- 0028H
- 0018H
- 0038H

37. 29. Address line for TRAP is

Mark only one oval.

- 0022H
- 0023H
- 0024H
- 0033H

38. 30. Which one has the highest priority

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RST 7.5

RST 6.5

RST 5.5

TRAP

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