

# Online Assessment (Even Sem/Part-I/Part-II Examinations 2019 - 2020)

Course Name - Microprocessors

Course Code - DECE 404

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Answer all the questions. Each question carry one mark.

9. 1. Consider the following registers: 1. Accumulator and flag register 2. B and C register 3. D and E register 4. H and L register Which of these 8-bit registers of 8085 microprocessor can be paired together to make a 16-bit register?

*Mark only one oval.*

- 1 ,3 and 4
- 2 ,3 and 4
- 1 ,2 and 4
- 1 ,2 and 3

10. 2. The 64 bit processor is

*Mark only one oval.*

- Pentium
- Pentium II
- Pentium III
- Pentium 4

11. 3. The address bus of 8085 microprocessor is

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- 16bit
- 20 bit
- 8 bit
- 24 bit

12. 4. ALU (Arithmetic and Logic Unit ) of 8085 microprocessor consists of

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- Accumulator, temporary register, arithmetic and logic circuits
- Accumulator, arithmetic, logic circuits and five flags
- Accumulator, arithmetic and logic circuits
- Accumulator, temporary register, arithmetic, logic circuits and five flags

13. 5. The program counter in a 8085 micro-processor is a 16-bit register, because

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- It counts 16-bits at a time
- There are 16 address lines
- It facilitates the user storing 16-bit data temporarily
- It has to fetch two 8-bit data at a time

14. 6. In an intel 8085A microprocessor, why is READY signal used?

*Mark only one oval.*

- To indicate to user that the microprocessor is working and is ready for use
- To provide proper WAIT states when the microprocessor is communicating with a slow peripheral device
- To slow down a fast peripheral device so as to communicate at the microprocessor's device
- None of these

15. 7. Consider the following I) Sign flag II) Trap flag III) Parity flag IV) Auxiliary carry flag  
Which one of the above flags is/are present in 8085 microprocessor?

*Mark only one oval.*

- (I) only
- (I) & (II)
- (II) & (III)
- (I) ,(III) & (IV)

16. 8. If the microprocessor is capable of addressing 64 Kbytes of memory, its address bus width is

*Mark only one oval.*

- 16 bits
- 20 bits
- 8 bits
- None of these

17. 9. A microprocessor performs as

*Mark only one oval.*

- CPU of a computer
- memory of a computer
- output device of a computer
- input device of a computer

18. 10. The number of flags of 8085 microprocessor is

*Mark only one oval.*

- 6
- 5
- 4
- 3

19. 11. READY signal in 8085 is useful when the CPU communicates with

*Mark only one oval.*

- a slow peripheral device
- a fast peripheral device
- a DMA chip
- a PPI

20. 12. Temporary registers in 8085 are

*Mark only one oval.*

- B and C
- D and E
- H and L
- W and Z

21. 13. Name of typical dedicated register is:

*Mark only one oval.*

- PC
- IR
- SP
- All of these

22. 14. The program counter in microprocessor

*Mark only one oval.*

- keeps the address of the next instruction to be fetched
- counts the numbers of instructions being executed on the microprocessor
- counts the number of program being executed on the microprocessor
- counts the number of interrupts handled by the microprocessor



23. 15. The data bus of microprocessor is

*Mark only one oval.*

- unidirectiona
- bi-directional
- unidirectional as well as bi-directional
- None of these

24. 16. What does microprocessor speed depends on?

*Mark only one oval.*

- Clock
- Data bus width
- Address bus width
- Control bus width

25. 17. What is the meaning of the instruction MVI A,05H?

*Mark only one oval.*

- data 05H is stored in the accumulator
- fifth bit of accumulator is set to one
- address 05H is stored in the accumulator
- None of these

26. 18. Which one of the following is non-maskable interrupt of 8085 microprocessor?

*Mark only one oval.*

RST 5.5

RST 6.5

TRAP

RST 5

27. 19. After execution of instruction RRC, which one of the following flag will be affected

*Mark only one oval.*

Auxiliary Flag

Zero Flag

Sign Flag

Carry Flag

28. 20. Number of software interrupts supported by 8085 microprocessor is

*Mark only one oval.*

7

6

8

9

29. 21. Which of the following are 16 bit register in 8051 microcontroller?

*Mark only one oval.*

- DPTR
- IE
- TMOD
- PC

30. 22. In 8051 which interrupt has highest priority?

*Mark only one oval.*

- IE1
- TF0
- IE0
- TF1

31. 23. 8086 has

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- 16 bit data bus and 20 bit address bus
- 8 bit data bus and 20 bit address bus
- 16 bit data bus and 16 bit address bus
- 8 bit data bus and 16 bit address bus

32. 24. In 8086, to execute a program, which command is used?

*Mark only one oval.*

R

G

E

F

33. 25. Clock frequency of 8086 and 8088 is

*Mark only one oval.*

5-10 MHz

2-3 MHz

1-3 MHz

2-5 MHz

34. 26. Memory map of 8086 is

*Mark only one oval.*

0000H to FFFFH

00000H to FFFFFFFH

0000H to 9FFFFH

None of these

35. 27. A two byte instruction consists of

*Mark only one oval.*

- opcode and operand
- opcode only
- opcode and two operands
- operand only

36. 28. What is meant by maskable interrupts?

*Mark only one oval.*

- an interrupt which can never be turned off
- an interrupt that can be turned off by the programmer
- an interrupt which can never be turned on
- an interrupt which can never be turned on or off

37. 29. MOV A, B is an instruction of

*Mark only one oval.*

- direct addressing mode
- indirect addressing mode
- register addressing mode
- Immediate addressing mode

38. 30. How many T-states are required for execution of OUT 80H instruction?

*Mark only one oval.*

10

13

16

7

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