

Online Assessment (Even Sem/Part-I/Part-II Examinations 2019 - 2020)

Course Name - Microprocessor and Microcontroller

Course Code - EC401(BL) (BSCCS/ BSCHN)

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Answer all the questions. Each question carry one mark.

9. 1. Which of the following instruction is immediate addressing?

Mark only one oval.

- MOV AX, [2000]
- MOV BX, 2000
- MOV AX, [SI]
- MOV AX, BX

10. 2. What is addressing mode of instruction MOV AX, [BX]?

Mark only one oval.

- register direct addressing mode
- immediate addressing mode
- register indirect addressing mode
- indirect addressing mode

11. 3. The instruction, MOV AX, 1234H is an example of

Mark only one oval.

- register addressing mode
- immediate addressing mode
- based indexed addressing mode
- direct addressing mode

12. 4. 8088 microprocessor has

Mark only one oval.

- 16 bit data bus
- 4 byte pre-fetch queue
- 6 byte pre-fetch queue
- 16 bit address bus

13. 5. Which of the following is based with 16 bit displacement addressing?

Mark only one oval.

- MOV AX, [BX+06]
- MOV AX, [BP+2000]
- MOV AX, [BP+06]
- MOV AX, [BP]

14. 6. A microprocessor performs as

Mark only one oval.

- CPU of a computer
- memory of a computer
- output device of a computer
- input device of a computer

15. 7. The data in the stack is called

Mark only one oval.

- Pushing data
- Pushed
- Pulling
- None of these

16. 8. Consider the following I) Sign flag II) Trap flag III) Parity flag IV) Auxiliary carry flag Which one of the above flags is/are present in 8085 microprocessor?

Mark only one oval.

- (I) only
- (I) & (II)
- (II) & (III)
- (I) ,(III) & (IV)

17. 9. How many address lines are required to access 1 MB RAM using microprocessor?

Mark only one oval.

- 16
- 8
- 20
- 12

18. 10. What is the meaning of the instruction MVI A,05H?

Mark only one oval.

- data 05H is stored in the accumulator
- fifth bit of accumulator is set to one
- address 05H is stored in the accumulator
- none of the mentioned

19. 11. What is SIM?

Mark only one oval.

- Select Interrupt Mask
- Shoting Interrupt Mask
- Set Interrupt Mask
- None of these

20. 12. Let 81H & C2H are added. The RAR is executed. The content of the Accumulator will be

Mark only one oval.

- 82H
- 87H
- 90H
- 97H

21. 13. HLT op-code means

Mark only one oval.

- 2 bytes
- 1 bytes
- 3 bytes
- 4 bytes

22. 14. Which instruction is required to rotate the content of accumulator one bit right along with carry?

Mark only one oval.

- RLC
 RAL
 RRC
 RAR

23. 15. STA 2070H is an instruction of

Mark only one oval.

- One byte
 two byte
 three byte
 four byte

24. 16. MOV A, B is an instruction of

Mark only one oval.

- direct addressing mode
 indirect addressing mode
 register addressing mode
 Immediate addressing mode

25. 17. LXI H, 2500 H is an instruction of

Mark only one oval.

- direct addressing mode
- indirect addressing mode
- register addressing mode
- Immediate addressing mode

26. 18. XCHG is an

Mark only one oval.

- data transfer instruction
- arithmetic instruction
- logical instruction
- I/O and stack pointer instruction

27. 19. What is RIM in context of microprocessor?

Mark only one oval.

- Ready Interrupt Mask
- Reading Interrupt Mask
- Read Interrupt Mask
- Read Integer Mask

28. 20. Instruction queue of 8088 consist of

Mark only one oval.

- 6 data
- 8 data
- 4 data
- 10 data

29. 21. Memory map of 8086 is

Mark only one oval.

- 0000H to FFFFH
- 00000H to FFFFFH
- 0000H to 9FFFH
- none of these

30. 22. The number of stop bits in case of asynchronous data transfer is

Mark only one oval.

- 2 bits
- 3 bits
- 4 bits
- 5 bits

31. 23. 16 bit register of 8086 consist of

Mark only one oval.

16 flags

8 flags

9 flags

7 flags

32. 24. Which flags of 8086 are not present in 8085?

Mark only one oval.

TF

DF

OF

All of these

33. 25. What is the size of instruction Queue of 8086 microprocessor?

Mark only one oval.

3

5

6

8

34. 26. In 8086 microprocessor, the size of each segment is

Mark only one oval.

32 KB

64 KB

16 KB

8 KB

35. 27. The Intel 8086 microprocessor is a _____ processor.

Mark only one oval.

8 bit

16 bit

32 bit

4 bit

36. 28. The 16 bit flag of 8086 microprocessor is responsible to indicate _____.

Mark only one oval.

the condition of result of ALU operation

the condition of memory

the result of addition

the result of subtraction

37. 29. In 8086 the overflow flag is set when _____.

Mark only one oval.

- NMI
- DIV 0
- TYPE 255
- OVER FLOW

38. 30. 8088 microprocessor has

Mark only one oval.

- 16 bit data bus
- 4 byte pre-fetch queue
- 6 byte pre-fetch queue
- 16 bit address bus

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