# Online Assessment (Even Sem/Part-I/Part-II Examinations 2019 - 2020

Course Name - Computer Organization and Architecture
Course Code - PCC-CS 401

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Answer all the questions. Each question carry one mark.

9. 1. Which of the following is a universal logic gate?

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- XOR
- OR
- NOR
- XNOR

10. 2. 2's complement of 10101011

*Mark only one oval.*

- 01011101
- 11010101
- 01010101
- 01110101

11. 3. The fastest data access can be obtained using

*Mark only one oval.*

- SRAM's
- DRAM's
- Caches
- Registers

12. 4. Von Neumann architecture is based on

*Mark only one oval.*

- stored program concept
- stored instruction concept
- stored data concept
- stored signal concept

13. 5. The format which is usually used to store data is computer

*Mark only one oval.*

- Decimal
- Octal
- BCD
- Hexadecimal

14. 6. DMA stands for

*Mark only one oval.*

- Discrete memory architecture
- Discrete memory access
- Direct memory architecture
- Direct memory access

15. 7. Which of the following is used to store intermediate result?

*Mark only one oval.*

- MAR
- MDR
- Accumulator
- Program Counter

16. 8. In full adders the sum circuit is implemented using

*Mark only one oval.*

- NOR
- XOR
- OR
- AND

17. 9. Which is not a data hazard

*Mark only one oval.*

- WAW
- RAW
- WAR
- RAR

18. 10. Boolean algebra is also known as

*Mark only one oval.*

- Switching Algebra
- Transistor Algebra
- Gate Algebra
- Counting Algebra

19. 11. Which of the following is not a bus?

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- address bus
- data bus
- control bus
- program bus

20. 12. Program always deals with

*Mark only one oval.*

- logical address
- physical address
- relative address
- absolute address

21. 13. Booth's algorithm is used for performing binary

*Mark only one oval.*

- addition
- division
- multiplication
- subtraction

22. 14. Floating point representation is used to store

*Mark only one oval.*

- Boolean values
- Real numbers
- Characters
- Integers

23. 15. Which of the following is used to store the address of next instruction?

*Mark only one oval.*

- Accumulator
- MAR
- Program Counter
- MDR

24. 16. In computers, subtraction is generally carried out by

*Mark only one oval.*

- 9’s complement
- 2’s complement
- 1's complement
- 10’s complement

25. 17. Which is a part of Flynn’s classification?

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- MIMD
- MISD
- SISD
- all

26. 18. RAM is

*Mark only one oval.*

- volatile
- non volatile
- both volatile and non volatile
- none

27. 19. Von Neumann architecture is

*Mark only one oval.*

- MISD
- MIMD
- SISD
- SIMD

28. 20. Total number of binary combinations if input is 'n'

*Mark only one oval.*

- n to the power n
- n to the power 2
- 2 to the power n
- 2 to the power 2

29. 21. Peripheral devices are

*Mark only one oval.*

- I/O devices
- Internal devices
- Any device
- CPU

30. 22. Write Through technique is used in which memory for updating the data?

*Mark only one oval.*

- Virtual memory
- Main memory
- Cache memory
- Auxiliary memory

31. 23. The instruction like ADD is called as

*Mark only one oval.*

- Operators
- OP-Code
- Command
- None

32. 24. MIMD stands for

*Mark only one oval.*

- Memory instruction multiple data
- Multiple instruction memory data
- Multiple instruction multiple data
- Multiple information multiple data

33. 25. SISD stands for

*Mark only one oval.*

- Sequence instruction single data
- Single information single data
- Single instruction sequence data
- Single instruction single data

34. 26. Address bus is

*Mark only one oval.*

- unidirectional
- bidirectional
- both unidirectional and bidirectional
- none

35. 27. Which of the following is/are part/s of Von Neumann architecture?

*Mark only one oval.*

- CU
- MU
- ALU
- all options

36. 28. Stage/s to execute an instruction in pipelining is/are

*Mark only one oval.*

- Fetch
- Decode
- Execute
- All options

37. 29. Which of the following is bidirectional?

*Mark only one oval.*

- Address bus
- Data Bus
- Control bus
- Program bus

38. 30. A group of 8 binary bits is called

*Mark only one oval.*

- Nibble
- Decimal
- Byte
- Digit