

Online Assessment (Even Sem/Part-I/Part-II Examinations 2019 - 2020)

Course Name - Microcontrollers

Course Code - PCC-EC403

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Answer all the questions. Each question carry one mark.

9. 1. Consider the following registers: 1. Accumulator and flag register 2. B and C register 3. D and E register 4. H and L register Which of these 8-bit registers of 8085 microprocessor can be paired together to make a 16-bit register?

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- 1 ,3 and 4
- 2 ,3 and 4
- 1 ,2 and 4
- 1 ,2 and 3

10. 2. The memory capacity of 8085 microprocessor is

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- 64 K
- 1 MB
- 16 MB
- 640 B

11. 3. The address bus of 8085 microprocessor is

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- 16 bit
- 20 bit
- 8 bit
- 24 bit

12. 4. ALU (Arithmetic and Logic Unit) of 8085 microprocessor consists of

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- Accumulator, temporary register, arithmetic and logic circuits
- Accumulator, arithmetic, logic circuits and five flags
- Accumulator, arithmetic and logic circuits
- Accumulator, temporary register, arithmetic, logic circuits and five flags

13. 5. Which of the following microprocessors is a 4 bit microprocessor?

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- 4004
- 8080
- 8085
- Z80

14. 6. In a computer where microprocessor is used as CPU is known as

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- Microprocessor
- Micro-computer
- Microcontroller
- All of these

15. 7. 8085 microprocessor has how many pins

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- 40
- 39
- 42
- 36

16. 8. READY signal in 8085 is useful when the CPU communicates with

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- a slow peripheral device
- a fast peripheral device
- a DMA chip
- a PPI

17. 9. Temporary registers in 8085 are

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- B and C
- D and E
- H and L
- W and Z

18. 10. Why 8085 processor is called an 8 bit processor?

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- because 8085 processor has 8 bit ALU
- because 8085 processor has 8 bit data bus
- because 8085 processor has 16 bit data bus
- because 8085 processor has 16 bit address bus

19. 11. Name of typical dedicated register is:

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- PC
- SP
- IR
- All of these

20. 12. The frequency of the driving network connected between pins 1 and 2 of 8085 microprocessor is

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- twice the desired frequency
- equal to the desired frequency
- four times the desired frequency
- None of these

21. 13. Both the ALU and control section of CPU employ which special purpose storage location?

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- Buffers
- Decoders
- Accumulators
- Registers

22. 14. A microprocessor is a

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- SSI device
- MSI device
- LSI device
- VLSI device

23. 15. Accumulator based microprocessor example are:

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- Intel 8085
- Motorola 6809
- Intel 8085 and Motorola 6809
- None of these

24. 16. The CALL location for TRAP is

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- 0023H
- 0032H
- 0042H
- 0024H

25. 17. Number of software interrupts supported by 8085 microprocessor is

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- 7
- 6
- 8
- 9

26. 18. The address / data bus in 8085 is _____

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- multiplexed
- demultiplexed
- decoded
- encoded

27. 19. MVI A, 58H is an instruction of

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- direct addressing mode
- indirect addressing mode
- register addressing mode
- immediate addressing mode

28. 20. Which of the following is a four byte instruction?

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- MOV AX, 2345
- MUL BX
- DIV CL
- ADD AX, [BP+0200]

29. 21. ADD C will be executed in

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- 1 Machine cycle
- 2 Machine cycle
- 3 Machine cycle
- 4 Machine cycle

30. 22. 16 bit register of 8086 consist of

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- 16 flags
- 8 flags
- 9 flags
- 7 flags

31. 23. HLT op-code means

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- Load data to accumulator
- Store result in memory
- Load accumulator with contents of register.
- End of program.

32. 24. DS Stand for

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- Data segment
- Direct segment
- Declare segment
- Divide segment

33. 25. Which of the following instructions is an arithmetic instruction?

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- DIV AB
- ROR
- STA
- WAIT

34. 26. The instruction format 'register to register' has a length of

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- 2 bytes
- 1 byte
- 3 bytes
- 4 bytes

35. 27. The instruction "JUMP" belongs to

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- sequential control flow instructions
- control transfer instructions
- branch instructions
- control transfer & branch instructions

36. 28. Which interrupt has the highest priority?

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- INTR
- TRAP
- RST6.5
- RST6.6

37. 29. What are level Triggering interrupts?

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- INTR & TRAP
- RST6.5 & RST5.5
- RST7.5 & RST6.5
- RST2.5 & RST6.2

38. 30. In 8086, which command is used to see the flag register status?

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R

G

E

F

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