



BRAINWARE UNIVERSITY

ODD Semester Examinations 2021- 22

Programme – Diploma in Computer Science & Engineering - 2018 [Dip.CSE]

Course Name – PC Hardware and Networking

Course Code – DCSE304

(Semester III)

Time allotted : 1 Hour 25 Minutes

Full Marks : 70

(Multiple choice type question)

70 x 1 = 70

Choose the correct alternative from the following

(I) Which Cache directly interacts with CPU

- A) L1
- B) L2
- C) L3
- D) None of these

(II) TCP/IP network model consist of _____ number of layers

- A) 5
- B) 7
- C) 6
- D) 8

(III) Which stage of instruction cycle is responsible for finding the addressing mode?

- A) Fetch
- B) Decode
- C) Execute
- D) Write Back

(IV) 1 Kilo Byte= _____ bits

- A) 1024
- B) 5536
- C) 8192
- D) 6616

(V) Which parallel processor has better throughput than pipeline processor?

- A) Intel 8085
- B) Super Pipeline
- C) VLIW
- D) both Super pipelining and VLIW

(VI) IR is responsible for

- A) Storing data
- B) Storing address
- C) Decoding address
- D) None of these

(VII) Number of characters supported in a file name in FAT file system used by MS-DOS

- A) 8
- B) 32
- C) 50
- D) 255

(VIII) Which of the following is an example of memory slot?

- A) SP
- B) SIMM
- C) ALU
- D) PCI

(IX) Which device helps to prevent congestion and data collisions?

- A) Hub
- B) Router
- C) Switch
- D) None of these

(X) Which is the most powerful partitioning technique?

- A) MBR
- B) GUID
- C) GPT
- D) All of these

(XI) What's a method of clearing the CMOS password?

- A) Send it back to the factory
- B) Flash the BIOS
- C) Change the clock to 00/00/0000
- D) Remove a jumper on the motherboard

(XII) Which partition can be subdivided into many parts

- A) Primary Partition
C) Logical Partition
- (XIII) Cache Memory is one type of
A) Secondary Memory
C) Auxiliary Memory
- (XIV) Which is not a chipset?
A) South bridge
C) Front Side Bus
- (XV) Intel 8085 has _____ bit Address bus
A) 8
C) 20
- (XVI) Who consumes less power?
A) SRAM
C) RDRAM
- (XVII) Which of the term is matched with SMPS?
A) Short Message Power Source
C) Switched Mode Power Source
- (XVIII) What is the full form of Pixel?
A) Picture Elements
C) Elements of Pictures
- (XIX) What is the shortcut key to select all?
A) Ctrl + A
C) Ctrl + V
- (XX) Which one is the most important partition from which booting starts?
A) Master Burst ROM
C) Master Boot Record
- (XXI) If a word cannot be found in cache memory, the incident is called a
A) Cache Hit
C) Cache Miss
- (XXII) The communication over telephone is an example of
A) simplex
C) Full-duplex
- (XXIII) Which register always stores the starting address of an instruction?
A) Program Counter
C) Stack Pointer
- (XXIV) Speed of a CPU is directly dependent on the width of
A) Address Bus
C) Control Bus
- (XXV) Which slot is mainly used to connect a graphics card?
A) Average Good Presentation
C) Accelerated Graphic Port
- (XXVI) Which one is the older one?
A) Itanium Series
C) CISC
- (XXVII) Accumulator belongs to which below class?
A) Register
C) CPU
- (XXVIII) In a memory, numbers of locations are 1024. What will be the bus width of the Address bus?
- B) Extended Partition
D) All of these
- B) Primary Memory
D) None of This
- B) North bridge
D) None of these
- B) 16
D) 24
- B) DRAM
D) SDRAM
- B) Switched Mode Power Supply
D) Short Message Power Supply
- B) Picture eyes
D) None of these
- B) Ctrl + C
D) Ctrl + X
- B) Master Boot ROM
D) None of these
- B) Cache Penalty
D) Both Cache Penalty and Cache Miss
- B) Half-Duplex
D) None of these
- B) Base
D) Limit
- B) Data Bus
D) None of these
- B) Accelerated Grant Processes
D) None of these
- B) Super Computer
D) Intel Itanium Series
- B) Memory
D) Expansion slot

- A) 5 bit
C) 10 bit
- B) 8 bit
D) 7 bit

(XXIX) Which is the most common computer keyboard layout?

- A) QWERTY
C) Devorak
- B) JCUKEN
D) Colemark

(XXX) Operating System is an example of

- A) System Software
C) Utility Software
- B) Application Software
D) All of these

(XXXI) The component of HDD responsible for rotating platters is called

- A) Track
C) Spindle
- B) Actuator
D) Cylinder

(XXXII) The first program runs when system starts is known as

- A) Assembly Program
C) Bootstrap Program
- B) System Program
D) None of these

(XXXIII) If a memory consist of 512 locations, the address bus width will be _____ bits

- A) 5
C) 10
- B) 9
D) 6

(XXXIV) In mesh topology, if number of node is n, then total number of link will be

- A) n
C) $n(n-1)/2$
- B) $2n$
D) $n(n+1)/2$

(XXXV) Intel 8085 has _____ bit data bus

- A) 8
C) 20
- B) 16
D) 24

(XXXVI) Which CPU technique gives $CPI < 1$?

- A) Pipelining
C) VLIW
- B) Super pipelining
D) both Super pipelining and VLIW

(XXXVII) Address of the top element of a stack memory is stored in

- A) SP
C) AC
- B) PC
D) BR

(XXXVIII) Which controller is the slowest one?

- A) IDE
C) SCSI
- B) SATA
D) None of these

(XXXIX) Which is not a part of CPU?

- A) ALU
C) CU
- B) SMPS
D) None of these

(XL) Which one is referred as LAN port?

- A) USB
C) Type-c
- B) RJ45
D) HDMI

(XLI) Data Bus is always

- A) Unidirectional
C) Direction Less
- B) Bidirectional
D) All of these

(XLII) Notepad is one kind of _____ software.

- A) Application
C) Utility
- B) System
D) None of these

(XLIII) Which gives better security?

- A) FAT
C) FAT 32
- B) FAT 16
D) NTFS

(XLIV) Which one is not present in Von-Neumann Computer?

- A) Registers
C) Cache
- B) Peripheral Devices
D) None of These
- (XLV) Intel 8085 has how many pins?
A) 14
C) 30
- B) 16
D) 40
- (XLVI) Cordless mouse or wireless mouse connects to the computer through
A) Infrared Light
C) Both
- B) Radio Wave
D) None
- (XLVII) OSI network model consist of _____ number of layers
A) 5
C) 6
- B) 7
D) 8
- (XLVIII) Which one is machine independent?
A) Machine Level Language
C) High Level Language
- B) Assembly Language
D) All of these
- (XLIX) Starting Address of next instruction is stored in
A) Program Counter
C) Base Register
- B) Instruction Register
D) Stack Pointer
- (L) Which file system DOS typically use?
A) FXZ
C) NTFS
- B) LMFS
D) FAT 16
- (LI) Southbridge is also known as
A) Northbridge
C) Peripheral Controller Hub
- B) Memory Controller Hub
D) FSB
- (LII) What is POST?
A) post operative self test
C) power-on self test
- B) PC operating system test
D) personal overall starter test
- (LIII) From what location are the 1st computer instructions available on boot up?
A) ROM BIOS
C) Boot.ini
- B) CPU
D) CONFIG.SYS
- (LIV) Flash memory is derived from
A) EPROM
C) PROM
- B) EEPROM
D) ROM
- (LV) Size of an MAC address is
A) 1 Byte
C) 4 Byte
- B) 2 Byte
D) 6 Byte
- (LVI) Interpreter falls into which category?
A) System Software
C) Utility Software
- B) Application Software
D) All of these
- (LVII) Address Bus is always
A) Unidirectional
C) Direction Less
- B) Bidirectional
D) All of these
- (LVIII) Which is not a part of CPU
A) CU
C) Register Set
- B) ALU
D) None of These
- (LIX) 192.168.1.1 is a _____ IP address
A) Class A
C) Class C
- B) Class A
D) Class C
- (LX) Maximum number of connecting wires are needed in

- A) Ring Topology
C) Mesh Topology
- (LXI) RIMM is a
A) Register
C) CPU
- (LXII) Which one requires a central hub?
A) Ring Topology
C) Mesh Topology
- (LXIII) Which is not a memory slot?
A) SIMM
C) RIMM
- (LXIV) IP addressing and subnetting is done in
A) Physical Layer
C) Transport Layer
- (LXV) Which of the below statement is true?
A) All Core 2 Duo are Dual Core Processors
C) Both (All Core 2 Duo are Dual Core Processors) and (All Dual Core Processors are Core 2 Duo)
- (LXVI) 4 Byte = _____ bit?
A) 16 bit
C) 26 bit
- (LXVII) Where do modern PCs store CMOS settings?
A) BIOS
C) CPU
- (LXVIII) Which one is true for a debugger?
A) System Software
C) Utility Software
- (LXIX) Which one is considered as an optical media for storage?
A) HDD
C) DVD ROM
- (LXX) Which of the following is an example of magnetic memory?
A) FDD
C) Both FDD and HDD
- B) Star Topology
D) Bus Topology
- B) Memory Slot
D) Expansion Slot
- B) Star Topology
D) Bus Topology
- B) DIMM
D) KIMM
- B) Network Layer
D) Application Layer
- B) All Dual Core Processors are Core 2 Duo
D) None of these
- B) 20 bit
D) 32 bit
- B) Flash ROM
D) NVRAM
- B) Application Software
D) All of these
- B) FDD
D) Register
- B) HDD
D) None of these