



BRAINWARE UNIVERSITY

ODD Semester Examinations 2021- 22

Programme – Diploma in Computer Science & Engineering - 2019 [Dip.CSE]

Course Name – PC Hardware and Networking

Course Code – DCSE304

(Semester III)

Time allotted : 1 Hour 15 Minutes

Full Marks : 60

(Multiple choice type question)

60 x 1 = 60

Choose the correct alternative from the following

- (I) Notepad is one kind of _____ software.
- A) Application
B) System
C) Utility
D) None of these
- (II) In mesh topology, if number of node is n, then total number of link will be
- A) n
B) 2n
C) $n(n-1)/2$
D) $n(n+1)/2$
- (III) JAVA is one type of
- A) Machine Language
B) Assembly Language
C) High Level Language
D) None of these
- (IV) From what location are the 1st computer instructions available on boot up?
- A) ROM BIOS
B) CPU
C) Boot.ini
D) CONFIG.SYS
- (V) Which partition can be subdivided into many parts
- A) Primary Partition
B) Extended Partition
C) Logical Partition
D) All of these
- (VI) Which is not a chipset?
- A) South bridge
B) North bridge
C) Front Side Bus
D) None of these
- (VII) Which among is not a type of printer?
- A) Dot Matrix
B) Inkjet
C) Laser
D) None of these
- (VIII) Which one requires a central hub?
- A) Ring Topology
B) Star Topology
C) Mesh Topology
D) Bus Topology
- (IX) Which is not a part of CPU
- A) CU
B) ALU
C) Register Set
D) None of These
- (X) If a memory consist of 512 locations, the address bus width will be _____ bits
- A) 5
B) 9
C) 10
D) 6
- (XI) The first program runs when system starts is known as
- A) Assembly Program
B) System Program
C) Bootstrap Program
D) None of these
- (XII) Which stage of instruction cycle is responsible for finding the addressing mode?

- A) Fetch
C) Execute
- (XIII) Which one is true for a debugger?
A) System Software
C) Utility Software
- (XIV) Which file system DOS typically use?
A) FXZ
C) NTFS
- (XV) Operating System is an example of
A) System Software
C) Utility Software
- (XVI) Which of the term is matched with SMPS?
A) Short Message Power Source
C) Switched Mode Power Source
- (XVII) Which of the following is an example of memory slot?
A) SP
C) ALU
- (XVIII) Where do modern PCs store CMOS settings?
A) BIOS
C) CPU
- (XIX) Cordless mouse or wireless mouse connects to the computer through
A) Infrared Light
C) Both
- (XX) RIMM is a
A) Register
C) CPU
- (XXI) Which is the most common computer keyboard layout?
A) QWERTY
C) Devorak
- (XXII) 4 Byte = _____ bit?
A) 16 bit
C) 26 bit
- (XXIII) The communication over telephone is an example of
A) simplex
C) Full-duplex
- (XXIV) Which one is the slowest one?
A) Magnetic tape
C) Optical Disc
- (XXV) Which Cache directly interacts with CPU
A) L1
C) L3
- (XXVI) Which of the below statement is true?
A) All Core 2 Duo are Dual Core Processors
C) Both (All Core 2 Duo are Dual Core Processors) and (All Dual Core Processors are Core 2 Duo)
- (XXVII) Intel 8085 has how many pins?
A) 14
C) 30
- B) Decode
D) Write Back
- B) Application Software
D) All of these
- B) LMFS
D) FAT 16
- B) Application Software
D) All of these
- B) Switched Mode Power Supply
D) Short Message Power Supply
- B) SIMM
D) PCI
- B) Flash ROM
D) NVRAM
- B) Radio Wave
D) None
- B) Memory Slot
D) Expansion Slot
- B) JCUKEN
D) Colemark
- B) 20 bit
D) 32 bit
- B) Half-Duplex
D) None of these
- B) HDD
D) Main Memory
- B) L2
D) None of these
- B) All Dual Core Processors are Core 2 Duo
D) None of these
- B) 16
D) 40

- (XXVIII) Which one is the older one?
- A) Itanium Series
 - B) Super Computer
 - C) CISC
 - D) Intel Itanium Series
- (XXIX) Address Bus is always
- A) Unidirectional
 - B) Bidirectional
 - C) Direction Less
 - D) All of these
- (XXX) Which one is not present in Von-Neumann Computer?
- A) Registers
 - B) Peripheral Devices
 - C) Cache
 - D) None of These
- (XXXI) Cache is treated as one of the _____ memories
- A) Primary
 - B) Secondary
 - C) Auxiliary
 - D) Both Secondary and Auxiliary
- (XXXII) OSI network model consist of _____ number of layers
- A) 5
 - B) 7
 - C) 6
 - D) 8
- (XXXIII) Which one is referred as LAN port?
- A) USB
 - B) RJ45
 - C) Type-c
 - D) HDMI
- (XXXIV) Which slot is mainly used to connect a graphics card?
- A) Average Good Presentation
 - B) Accelerated Grant Processes
 - C) Accelerated Graphic Port
 - D) None of these
- (XXXV) IP addressing and subnetting is done in
- A) Physical Layer
 - B) Network Layer
 - C) Transport Layer
 - D) Application Layer
- (XXXVI) Satellite communication is needed in case of
- A) LAN
 - B) MAN
 - C) WAN
 - D) None of these
- (XXXVII) 1 Kilo Byte= _____ bits
- A) 1024
 - B) 5536
 - C) 8192
 - D) 6616
- (XXXVIII) 192.168.1.1 is a _____ IP address
- A) Class A
 - B) Class A
 - C) Class C
 - D) Class C
- (XXXIX) Which parallel processor has better throughput than pipeline processor?
- A) Intel 8085
 - B) Super Pipeline
 - C) VLIW
 - D) both Super pipelining and VLIW
- (XL) TCP/IP network model consist of _____ number of layers
- A) 5
 - B) 7
 - C) 6
 - D) 8
- (XLI) What is POST?
- A) post operative self test
 - B) PC operating system test
 - C) power-on self test
 - D) personal overall starter test
- (XLII) Number of characters supported in a file name in FAT file system used by MS-DOS
- A) 8
 - B) 32
 - C) 50
 - D) 255
- (XLIII) What's a method of clearing the CMOS password?
- A) Send it back to the factory
 - B) Flash the BIOS
 - C) Change the clock to 00/00/0000
 - D) Remove a jumper on the motherboard

- (XLIV) Accumulator belongs to which below class?
A) Register
B) Memory
C) CPU
D) Expansion slot
- (XLV) The component of HDD responsible for rotating platters is called
A) Track
B) Actuator
C) Spindle
D) Cylinder
- (XLVI) What is the full form of UEFI
A) Uniform Enhanced Firewall Internet
B) Uniform Extensible Firmware Interface
C) Uniform Enhanced Firmware Interconnect
D) None of these
- (XLVII) Who consumes less power?
A) SRAM
B) DRAM
C) RDRAM
D) SDRAM
- (XLVIII) What is the full form of Pixel?
A) Picture Elements
B) Picture eyes
C) Elements of Pictures
D) None of these
- (XLIX) Any web browser like internet explorer, Google chrome is an example of
A) System Software
B) Application Software
C) Utility Software
D) All of these
- (L) Which CPU technique gives CPI < 1?
A) Pipelining
B) Super pipelining
C) VLIW
D) both Super pipelining and VLIW
- (LI) In a memory, numbers of locations are 1024. What will be the bus width of the Address bus?
A) 5 bit
B) 8 bit
C) 10 bit
D) 7 bit
- (LII) Which of the following network topology is most advantageous?
A) Bus Topology
B) Ring Topology
C) Star Topology
D) Hybrid Topology
- (LIII) Maximum number of connecting wires are needed in
A) Ring Topology
B) Star Topology
C) Mesh Topology
D) Bus Topology
- (LIV) Speed of a CPU is directly dependent on the width of
A) Address Bus
B) Data Bus
C) Control Bus
D) None of these
- (LV) Flash memory is derived from
A) EPROM
B) EEPROM
C) PROM
D) ROM
- (LVI) Which one is generally used to built cache memory?
A) SRAM
B) DRAM
C) RDRAM
D) SDRAM
- (LVII) Which device helps to prevent congestion and data collisions?
A) Hub
B) Router
C) Switch
D) None of these
- (LVIII) IR is responsible for
A) Storing data
B) Storing address
C) Decoding address
D) None of these
- (LIX) Intel 8085 has _____ bit Address bus
A) 8
B) 16
C) 20
D) 24

(LX) If a word cannot be found in cache memory, the incident is called a

A) Cache Hit

B) Cache Penalty

C) Cache Miss

D) Both Cache Penalty and Cache Miss