



BRAINWARE UNIVERSITY

Course – MSc. (HN)

Core Hardware (MHN103)

(Semester – 1)

Time allotted: 3 Hours

Full Marks: 70

[The figure in the margin indicates full marks. Candidates are required to give their answers in their own words as far as practicable.]

Group –A

(Multiple Choice Type Questions)

10 x 1 = 10

1. *Choose the correct alternative from the following*
 - (i) Maximum number of primary partitions, supported by MBR, is
 - a. 4
 - b. 24
 - c. 127
 - d. 128
 - (ii) Colour of +12 V DC voltage in SMPS is
 - a. Red
 - b. Blue
 - c. Green
 - d. None of these
 - (iii) What is the type of IDE translation?
 - a. LBA
 - b. CHS
 - c. USB
 - d. None of these
 - (iv) The 'North Bridge Chip' is popularly known as _____
 - a. GMCH
 - b. GCMH
 - c. MCH
 - d. MHC
 - (v) Which IRQ does the system timer use?
 - a. IRQ 0
 - b. IRQ 1
 - c. IRQ 2
 - d. IRQ 3

8. (a) What is 'Cache Memory'? [5]
- (b) Explain the usefulness of 'High Level Formatting'. [10]
9. Describe the different RAID levels, used for fault tolerance. [15]
10. Explain the different types of ROM. [15]
11. (a) What is 'Chipsets'? [5]
- (b) Explain the 'Form Factor' and 'Super I/O Chip'. [10]