



BRAINWARE UNIVERSITY

Term End Examination 2018 - 19

Programme – MSc in Hardware & Networking

Course Name – Core Hardware

Course Code – MHNE010402

(Semester – 1)

Time allotted: 3 Hours

Full Marks : 70

[The figure in the margin indicates full marks. Candidates are required to give their answers in their own words as far as practicable.]

Group –A

(Multiple Choice Type Question)

10 x 1 = 10

1. *Choose the correct alternative from the following*
 - (i) From what location are the 1st computer instructions available on boot up?
 - a. ROM BIOS
 - b. CPU
 - c. BOOT.INI
 - d. CONFIG.SYS
 - (ii) Missing slot covers on a computer can cause
 - a. Over heat
 - b. Over heat
 - c. EMI
 - d. EMI
 - (iii) How many pins on a VGA?
 - a. 15
 - b. 9
 - c. 25
 - d. 32
 - (iv) What is modified when changing the system start-up boot sequence?
 - a. BIOS/CMOS
 - b. CONFIG.SYS
 - c. AUTOEXE.BAT
 - d. COMMAND.COM
 - (v) Which device is on IRQ 6?
 - a. Keyboard
 - b. IDE
 - c. Floppy drive
 - d. Video card

- (vi) Which file system DOS typically use?
- | | |
|----------|----------|
| a. FAT16 | b. FAT32 |
| c. NTFS | d. LMFS |
- (vii) Which of the following is default mode for Windows to start?
- | | |
|-------------------|----------------------|
| a. Command prompt | b. Safe mode |
| c. Normal mode | d. None of the above |
- (viii) A storage devices where the access time Is effectively independent of the location of the data is referred as:
- | | |
|--------------------|--------------------|
| a. Direct access | b. Direct access |
| c. Primary storage | d. Primary storage |
- (ix) Central Processing Unit (CPU) consists of
- | | |
|-----------------|-----------------|
| a. Control unit | b. Control unit |
| c. Main store | d. Main store |
- (x) Devices that are under control of computer and are directly connected to computer peripherals are termed as
- | | |
|---------------------|---------------------|
| a. Off-line devices | b. Off-line devices |
| c. IN gate device | d. IN gate device |

Group – B

(Short Answer Type Questions)

3 x 5 = 15

Answer any *three* from the following.

2. What is the difference between CISC and RISC? [5]
3. Which file is responsible for stored windows 7 booting information? Differentiate Full POST and Mini POST. [5]
4. Which file is responsible for windows virtual memory? Discuss about SIMM and DIMM and RIMM. [5]
5. Difference between ROM and RAM. [5]
6. What is the concept of Virtual memory? [5]

Group – C

(Long Answer Type Questions)

3 x 15 = 45

Answer any *three* from the following

7. (a) What is the difference between low-level format and high level format? [5]
(b) Discuss about ARC path in Boot.ini file. [5]
(c) Discuss how to diagnosis blue screen of death error. [5]
8. (a) How could you solve NTLDR missing problem in windows XP? [5]
(b) What is the role of MBR? [5]
(c) How could you solve BOOT.INI missing problem? [5]
9. (a) What is the concept of data bus address bus and control bus.? [5]
(b) Discuss about the different types of ROM. [10]
10. (a) What is the concept of NVRAM? [5]
(b) Discuss about the types of Motherboard. [5]
(c) What is the difference between CIMOS and BIOS? [5]
11. (a) How does the Zone bit recording influence the data storage in hard disk? [5]
(b) Differentiate Core2Duo and Dual core processor. [5]
(c) Differentiate Static RAM and Dynamic RAM. [5]
-