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BRAINWARE UNIVERSITY

Term End Examination 2024-2025 Programme – B.Sc.(ANCS)-Hons-2023/B.Sc.(ANCS)-Hons-2024 Course Name – Fundamentals of Electronics Course Code - BNC10001 (Semester I)

	The figure in the margin indicates full marks. Cand own words as far		answers in their
		Para later and authorities and artist	
	Grou	p-A	
1.	(Multiple Choice Choose the correct alternative from the following		1 x 15=15
(i)	Identify is the primary function of a resistor in an	electronic circuit?	
(ii)	a) To amplify signals c) To provide resistance to the flow of current Calculate the current flowing through a 220-ohm using Ohm's Law (V = IR).		volt power supply
(iii)	a) 0.0545 Ac) 5.0 AChoose the basic property of a semiconductor ma	b) 2.73 A d) 16.4 A terial?	
(iv)	a) High conductivity c) Intermediate conductivity Identify the semiconductor device which is comm	d) Perfect insulating properties	
(v)	a) Transistorc) CapacitorChoose the correct alternative of zener diode in ca	b) Diode d) Resistor ase of voltage regulation	
(vi)	a) Operational Amplifierc) Integrated CircuitsRecognize the advantages of a junction transistor	b) MOSFET d) None of these over the vacuum triode is	·
(vii)	a) high power consumptionc) large sizeDetermine in an NPN transistor symbol, the arrow	b) High efficiencyd) Less dopingis pointed towards	
(viii)	a) the collector c) the emitter The transfer of a signal in a transistor is	b) The base d) Depends on the configuratio	on
	a) low to high resistance c) collector to base junction	b) High to low resistanced) Emitter to base junction	

(ix)	Determine the octal equivalent	of the binary number: 10111101					
(1//)		ь) 275					
	a) 675 c) 572	d) 573					
(x)	C/ 3/2						
	How many AND gates are required to construct the Boolean expression,?						
	a) 1	b) 2					
	-1 2	d) 4					
(xi)							
,	a) "0"	b) "1"	is Mis				
	c) Fither "0" or "1"	d) None of these	y Barasar				
(xii)	The binary equivalent of the de	cimal number 10 is	37,100,15,2				
(,	a) 1010	b) 10101 Brainishop Ben	gar.				
	c) 10011	d) None of these 398, Karla, West					
(xiii)	Determine the select lines of 1	b) "1" d) None of these cimal number 10 is b) 10101 d) None of these 6 to 1 multiplexer					
	a) 4	b) 3					
	c) 5	d) 1					
(xiv)	Calculate minimum number of	4-to-1 multiplexers required to realize a 16-to-1 multiple	exer is				
(,	a) 3	b) 4					
	c) 5	3 (d) 8 (1)					
(xv)	When an inventer is placed be	tween the inputs of an S-R flip-flop for creating a new fli	p-flop				
	which is known as	के कुल्कार्थ । जिल्लाका विकास के किल्लाका के किल्लाका के किल्लाका के किल्लाका के किल्लाका के किल्लाका के किल्ला के किल्लाका के किल्लाका क					
	a) J-K flip-flop	b) master-slave flip-flop					
	c) T flip-flop	d) D flip-flop					
		Group-B					
		(Short Answer Type Questions) 3	x 5=15				
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	Write the applications of BJT?	an edilor of the main high local or the transfer	(3)				
	-	ge of 12V and a resistor of 100 ohms, can you calculate the	(3)				
	power dissipation?						
1	(a) Identify the decimal equivale	nt number of an octal number 512.	(3)				
		t number of an octal number 736.	, ,				
	(b) racinary and binary equivalent	promote de la companya della companya della companya de la companya de la companya della company					
		also views and the second second					
5.	Apply Boolean algebra to simp	olify the expression	(3)				
•	rippiy Boolean algebra to tamp						
	$Y = \left(\overline{A} + B\right) \cdot \left(A + B\right)$						
6.	Discriminates between combinat	ional circuit and sequential circuit.	(3)				
			•				
		OR					
	Discriminate between latch & flip	0	(3)				

Group-C

(Long Answer Type Questions) 5 >	x 6=30
7. Explain Commutative law, Associative law, Distributive law related to Boolean algebra.	(5)
8. Explain three terminals of a transistor.	(5)
 In a common base connection, current amplification factor is 0.9. If the emitter current is 1mA, determine the value of base current. 	(5)
10. Describe NOR gate and demonstrate the action of NOR gate as Universal gate.	(5)
11. Compare Zener breakdown and Avalanche breakdown.	(5)
12. Explain half subtractor with its block diagram, truth table and circuit diagram by using logic gates.	(5)
explain Full-Adder. OR Explain Full-Adder.	(5)
