



## **BRAINWARE UNIVERSITY**

## Term End Examination 2024-2025 Programme – M.Tech.(CSE)-AIML-2024 Course Name – Operating Systems and Virtualization

Course Code - MTA10104

(Semester I)

Library
Brainware University
398, Ramkrishnapur Road, Barasat
Kolkata, West Bengal-700125

Full Marks: 60

Time: 2:30 Hours

[The figure in the margin indicates full marks. Candidates are required to give their answers in their own words as far as practicable.]

Group-	roup-A
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(Multiple Choice Type Question)

1 x 15=15

	(iviuitipie Choice			
1.	Choose the correct alternative from the following	g:		
(i)	Select the advantage of using a multi-processor system.			
(ii)	a) Faster disk I/O b) Improved reliability and performance c) Lower power consumption d) Simplified system architecture (ii) Select Correct option: Inter-Processor Interrupts (IPI) in multi-processor systems are used to			
	a) Switch tasks between processors	b) Allow one processor to interrupt another for communication or task coordination		
(iii)	<ul> <li>c) Transfer control of memory management between processors</li> <li>Identify the technique to avoid the deadlock in a</li> </ul>	d) Handle input/output operations between processors a distributed system.		
(iv)	<ul><li>a) Preemptive Scheduling</li><li>c) Token Passing</li><li>Identify the protocol that is commonly used for</li></ul>	b) Time Stamps d) Priority Inversion communication in distributed systems		

a) TCP/IP b)

b) ARP

c) Ethernet

- d) CSMA/CD
- (v) Choose factor that is the most crucial for video streaming in a multimedia file system.
  - a) High storage capacity

- b) Low disk fragmentation
- c) Low latency and high data throughput
- d) Data compression techniques
- (vi) Choose correct option: File migration of a Distributed File System typically occurs in\_\_\_\_\_
  - a) A node becomes unavailable
- b) The system needs to balance load across servers
- c) Cache coherence is violated
- d) Files need to be deleted from the system
- (vii) Choose primarily used techniques to reduce power consumption in mobile devices.
  - a) Overclocking the CPU

b) Increasing screen brightness

c) Using multiple active SIM cards	(DVFS)	aling	
(viii) Choose concurrency control algorithms that use	es timestamps to order transactions.		
a) Two-phase locking (2PL) c) Optimistic concurrency control (ix) Select true statement about deadlocks.	<ul><li>b) Timestamp ordering</li><li>d) Multiversion concurrency control (</li></ul>	MVCC)	
<ul><li>a) Deadlocks occur only in single-user database systems.</li><li>c) Deadlocks are always caused by hardware</li></ul>	<ul> <li>b) Deadlocks can be avoided using the phase locking protocol.</li> <li>d) Deadlocks are resolved by schedul transactions in a random order.</li> </ul>		
failure.  (x) Choose the method to prevent concurrent transin a database.  a) Indexing	b) Locking d) Sorting		
<ul><li>c) Caching</li><li>(xi) Select a type of locking mechanism that is NOT</li></ul>	applicable in database management.		
a) Shared lock c) Recursive lock (xii) Select the method used to detect deadlocks in	b) Exclusive lock d) Intent lock Brainware I	Library Brainware University 398, Ramkrishnapur Road, Barasa Kolkata, West Bengal-700125	
<ul> <li>a) Time stamping</li> <li>c) Optimistic concurrency control</li> <li>(xiii) Choose concurrency control algorithm that allo locking resources but checks for conflicts at the</li> </ul>	d) Rollback ws transactions to proceed without	nqal-700125	
a) Two-phase locking c) Optimistic concurrency control (xiv) Identify key challenge to implement virtual mer	<ul><li>b) Time-stamp ordering</li><li>d) Strict two-phase locking</li></ul>		
<ul><li>a) Handling multiple cache hierarchies</li><li>c) Maintaining TLB coherence</li><li>(xv) Select main advantage of using a cache in a mu</li></ul>	<ul><li>b) Process synchronization</li><li>d) Managing shared memory regions</li><li>ltiprocessor system.</li></ul>		
<ul><li>a) Reduces memory usage</li><li>c) Increases CPU clock speed</li></ul>	b) Decreases the number of processed) Reduces memory access latency	s	
<b>Gro</b> u (Short Answer T		3 x 5=15	
<ol> <li>Define consistency model in shared memory system</li> <li>Write key requirements of a database operating states</li> <li>Explain mutual exclusion in distributed operating states</li> <li>Distinguish between ARM and Intel processors with</li> </ol>	ystem. system.	(3) (3) (3)	
mobile devices.  6. Explain the key components of a typical mobile op  O	perating system architecture.	(3)	
In respect of native-level programming compare A		(3)	
Grou	ın-C		
(Long Answer Ty	-	5 x 6=30	
<ol> <li>Describe the various strategies used to handle de</li> <li>Explain the role of the kernel in mobile operating</li> <li>Compare and contrast pessimistic and optimistic relation to their synchronization mechanisms.</li> </ol>	g systems to manage hardware resour concurrency control techniques in	(5) rces. (5) (5)	
10. Differentiate between centralized and distributed 11. Explain common runtime issues faced by mobile	d transaction processing models. applications.	(5) (5)	

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