



BRAINWARE UNIVERSITY

Term End Examination 2020 - 21

Programme – Diploma in Computer Science & Engineering

Course Name – PC Hardware and Networking

Course Code - DCSE304

Semester / Year - Semester III

Time allotted : 85 Minutes

Full Marks : 70

[The figure in the margin indicates full marks. Candidates are required to give their answers in their own words as far as practicable.]

Group-A

(Multiple Choice Type Question)

1 x 70=70

1. (Answer any Seventy)

(i) Starting Address of next instruction is stored in

- | | |
|--------------------|-------------------------|
| a) Program Counter | b) Instruction Register |
| c) Base Register | d) Stack Pointer |

(ii) Device drivers are the examples of _____ softwares

- | | |
|----------------|----------------|
| a) Application | b) System |
| c) Utility | d) Server side |

(iii) Which is not a part of CPU?

- | | |
|--------|------------------|
| a) ALU | b) SMPS |
| c) CU | d) None of these |

(iv) Which one is the fastest one?

- | | |
|-------------|----------------|
| a) Cache | b) HDD |
| c) Register | d) Main Memory |

(v) Von-Neumann Computer is also known as

- | | |
|-------------------|-------------------|
| a) ISA Computer | b) Batch Computer |
| c) Super Computer | d) All of these |

(vi) Base Register always stores the _____

- a) Starting Address
- b) Size
- c) Ending Address
- d) None of These

(vii) Address of the top element of a stack memory is stored in

- a) SP
- b) PC
- c) AC
- d) BR

(viii) Which of the following is an example of application software?

- a) Compiler
- b) Interpreter
- c) Both (Compiler) and (Interpreter)
- d) None of these

(ix) If a memory consist of 512 locations, the address bus width will be _____ bits

- a) 5
- b) 9
- c) 10
- d) 6

(x) The purpose of limit register is to store

- a) Starting Address
- b) Next Address
- c) Current Address
- d) Size

(xi) Notepad is one kind of _____ software.

- a) Application
- b) System
- c) Utility
- d) None of these

(xii) Transistor is used in _____ generation computers

- a) First
- b) Second
- c) Third
- d) Fourth

(xiii) Which is the older one?

- a) RISC
- b) CISC

c) Mainframe

d) Super Computer

(xiv) 4 Byte = _____ bit?

a) 16 bit

b) 20 bit

c) 26 bit

d) 32 bit

(xv) Which register always stores the starting address of an instruction?

a) Program Counter

b) Base

c) Stack Pointer

d) Limit

(xvi) Which is the most common computer keyboard layout?

a) QWERTY

b) JCUKEN

c) Devorak

d) Colemark

(xvii) Which of the following type of keyboard introduced the SysRq (System Request) key?

a) XT

b) AT

c) Extended

d) Enhanced

(xviii) What is the shortcut key to select all?

a) Ctrl + A

b) Ctrl + C

c) Ctrl + V

d) Ctrl + X

(xix) Address Bus is always

a) Unidirectional

b) Bidirectional

c) Direction Less

d) All of these

(xx) What is the full form of GPT?

a) Grant Partition Table

b) GUID Partition Table

c) Grade Partition Table

d) None of these

(xxi) A hard disk is divided into tracks which are further subdivided into

- a) Vectors
- b) Sectors
- c) Clusters
- d) Cylinders

(xxii) Data Bus is always

- a) Unidirectional
- b) Bidirectional
- c) Direction Less
- d) All of these

(xxiii) What chemical is used to form the storage layer of a platter of HDD?

- a) Ferric Oxide
- b) Cobalt Alloy
- c) Aluminum
- d) both Ferric Oxide and Cobalt Alloy

(xxiv) Which is not a valid file system?

- a) FAT 16
- b) FAT32
- c) NTFS
- d) FXZ

(xxv) Which is not a Hard Disk Controller?

- a) SATA
- b) PATA
- c) SCSI
- d) None of these

(xxvi) Which partition can be subdivided into many parts

- a) Primary Partition
- b) Extended Partition
- c) Logical Partition
- d) All of these

(xxvii) Integrated Circuits are the main ingredient of

- a) RISC
- b) CISC
- c) Both
- d) Only Super Computer

(xxviii) Which is not a memory slot?

- a) SIMM
- b) DIMM

c) RIMM

d) KIMM

(xxix) What is the full form of UEFI

a) Uniform Enhanced Firewall Internet

b) Uniform Extensible Firmware Interface

c) Uniform Enhanced Firmware Interconnect

d) None of these

(xxx) Which chip can be reprogrammed to update its contents?

a) System BIOS

b) ROM

c) RAM

d) Flash ROM

(xxxii) What's a method of clearing the CMOS password?

a) Send it back to the factory

b) Flash the BIOS

c) Change the clock to 00/00/0000

d) Remove a jumper on the motherboard

(xxxii) What is POST?

a) post operative self test

b) PC operating system test

c) power-on self test

d) personal overall starter test

(xxxiii) Who stores binary information as a charge applied to capacitors?

a) SRAM

b) DRAM

c) RDRAM

d) SDRAM

(xxxiv) Which is the costliest one?

a) SRAM

b) DRAM

c) RDRAM

d) SDRAM

(xxxv) Which has higher packaging density?

a) SRAM

b) DRAM

c) RDRAM

d) SDRAM

(xxxvi) Which CPU technique gives $CPI < 1$?

- a) Pipelining
- b) Super pipelining
- c) VLIW
- d) both Super pipelining and VLIW

(xxxvii) Which parallel processor has better throughput than pipeline processor?

- a) Intel 8085
- b) Super Pipeline
- c) VLIW
- d) both Super pipelining and VLIW

(xxxviii) Which slot is mainly used to connect a graphics card?

- a) Average Good Presentation
- b) Accelerated Grant Processes
- c) Accelerated Graphic Port
- d) None of these

(xxxix) If a word cannot be found in cache memory, the incident is called a

- a) Cache Hit
- b) Cache Penalty
- c) Cache Miss
- d) Both Cache Penalty and Cache Miss

(xl) Only memory, whose speed can be equivalent to CPU is

- a) Cache Memory
- b) Main Memory
- c) Registers
- d) None of these

(xli) Speed of a CPU is directly dependent on the width of

- a) Address Bus
- b) Data Bus
- c) Control Bus
- d) None of these

(xlii) If CPU finds a desired word in cache, that incident is called

- a) Cache Miss
- b) Cache Hit
- c) Cache Penalty
- d) None of these

(xliii) Which stage of instruction cycle is only executed by ALU, not CU

- a) Fetch
- b) Decode

c) Execute

d) Write Back

(xliv) Which of the below statement is true?

- a) All Core 2 Duo are Dual Core Processors b) All Dual Core Processors are Core 2 Duo
c) Both (All Core 2 Duo are Dual Core Processors) and (All Dual Core Processors are Core 2 Duo)
d) None of these

(xlv) Which of the Bus is/are bidirectional?

- a) Address Bus b) Control Bus
c) Data Bus d) Both (Address Bus) and (Control Bus)

(xlvi) Which is not a chipset?

- a) South bridge b) North bridge
c) Front Side Bus d) None of these

(xlvii) ATX motherboard consist of _____ numbers of pins

- a) 20 b) 16
c) 40 d) 24

(xlviii) In a SMPS, Ground is indicated by _____ colored wire

- a) Red b) Green
c) Black d) Blue

(xlix) Southbridge is also known as

- a) Northbridge b) Memory Controller Hub
c) Peripheral Controller Hub d) FSB

(l) Which of the following is associated with motherboard technology?

- a) Advanced Technology b) Accelerated Technology
c) Accelerated Technique d) None of these

(li) The part of a motherboard made with solid steel is known as
a) CPU
b) Graphics Processor
c) Cache Memory
d) None of these

(lii) Which one is an output device?
a) Printer
b) Monitor
c) Both (Printer) and (Monitor)
d) None of these

(liii) Which among is not a type of printer?
a) Dot Matrix
b) Inkjet
c) Laser
d) None of these

(liv) OSI network model consist of _____ number of layers
a) 5
b) 7
c) 6
d) 8

(lv) TCP/IP network model consist of _____ number of layers
a) 5
b) 7
c) 6
d) 8

(lvi) In a class A IP address, Network ID is determined by
a) First Byte
b) Second Byte
c) Third Byte
d) Fourth Byte

(lvii) Starting with 110 as first octet indicates which class IP Address?
a) Class A
b) Class B
c) Class C
d) Class D

(lviii) IP addressing and subnetting is done in
a) Physical Layer
b) Network Layer

c) Transport Layer

d) Application Layer

(lix) Size of an IPV4 address is

a) 1 Byte

b) 2 Byte

c) 3 Byte

d) 4 Byte

(lx) The communication over telephone line is an example of _____ communication

a) Serial

b) Parallel

c) Both of these

d) None of these

(lxi) Maximum number of connecting wires are needed in

a) Ring Topology

b) Star Topology

c) Mesh Topology

d) Bus Topology

(lxii) In a building few computers are connected to form a

a) LAN

b) MAN

c) WAN

d) None of these

(lxiii) In mesh topology, if number of node is n, then total number of link will be

a) n

b) 2n

c) $n(n-1)/2$

d) $n(n+1)/2$

(lxiv) Size of an IPV6 address is of

a) 32 bits

b) 48 bits

c) 64 bits

d) 128 bits

(lxv) Which device is used to boost the signal between two wireless access points?

a) Hub

b) Router

c) Switch

d) Booster

(lxvi) The device that provides central connection point for cables

a) Router

b) Hub

c) Gateway

d) Repeater

(lxvii) Which one is referred as LAN port?

a) USB

b) RJ45

c) Type-c

d) HDMI

(lxviii) Which device helps to prevent congestion and data collisions?

a) Hub

b) Router

c) Switch

d) None of these

(lxix) Which of the following network topology is most advantageous?

a) Bus Topology

b) Ring Topology

c) Star Topology

d) Hybrid Topology

(lxx) In CAT5/CAT6 type cable, how many wires are there?

a) 2

b) 6

c) 8

d) 15