



BRAINWARE UNIVERSITY

Term End Examination 2020 - 21

Programme – Diploma in Computer Science & Engineering

Course Name – PC Hardware and Networking

Course Code - DCSE304

Semester / Year - Semester III

Time allotted : 75 Minutes

Full Marks : 60

[The figure in the margin indicates full marks. Candidates are required to give their answers in their own words as far as practicable.]

Group-A

(Multiple Choice Type Question)

1 x 60=60

1. (Answer any Sixty)

(i) Device drivers are the examples of _____ softwares

- | | |
|----------------|----------------|
| a) Application | b) System |
| c) Utility | d) Server side |

(ii) Which is not a part of CPU?

- | | |
|--------|------------------|
| a) ALU | b) SMPS |
| c) CU | d) None of these |

(iii) Which one is the fastest one?

- | | |
|-------------|----------------|
| a) Cache | b) HDD |
| c) Register | d) Main Memory |

(iv) Base Register always stores the _____

- | | |
|---------------------|------------------|
| a) Starting Address | b) Size |
| c) Ending Address | d) None of These |

(v) Which one is true for a debugger?

- | | |
|---------------------|-------------------------|
| a) System Software | b) Application Software |
| c) Utility Software | d) All of these |

(vi) Address of the top element of a stack memory is stored in

- a) SP
- b) PC
- c) AC
- d) BR

(vii) If a memory consist of 512 locations, the address bus width will be _____ bits

- a) 5
- b) 9
- c) 10
- d) 6

(viii) Transistor is used in _____ generation computers

- a) First
- b) Second
- c) Third
- d) Fourth

(ix) AI is implemented in _____ generation computer

- a) First
- b) Second
- c) Fourth
- d) Fifth

(x) 4 Byte = _____ bit?

- a) 16 bit
- b) 20 bit
- c) 26 bit
- d) 32 bit

(xi) Which register always stores the starting address of an instruction?

- a) Program Counter
- b) Base
- c) Stack Pointer
- d) Limit

(xii) Cordless mouse or wireless mouse connects to the computer through

- a) Infrared Light
- b) Radio Wave
- c) Both
- d) None

(xiii) What is the shortcut key to select all?

- a) Ctrl + A
- b) Ctrl + C

c) Ctrl + V

d) Ctrl + X

(xiv) Address Bus is always

a) Unidirectional

b) Bidirectional

c) Direction Less

d) All of these

(xv) Which is the most powerful partitioning technique?

a) MBR

b) GUID

c) GPT

d) All of these

(xvi) What is the full form of GPT?

a) Grant Partition Table

b) GUID Partition Table

c) Grade Partition Table

d) None of these

(xvii) A hard disk is divided into tracks which are further subdivided into

a) Vectors

b) Sectors

c) Clusters

d) Cylinders

(xviii) Data Bus is always

a) Unidirectional

b) Bidirectional

c) Direction Less

d) All of these

(xix) What chemical is used to form the storage layer of a platter of HDD?

a) Ferric Oxide

b) Cobalt Alloy

c) Aluminum

d) both Ferric Oxide and Cobalt Alloy

(xx) Which is not a Hard Disk Controller?

a) SATA

b) PATA

c) SCSI

d) None of these

(xxi) Which partition can be subdivided into many parts

- a) Primary Partition
- b) Extended Partition
- c) Logical Partition
- d) All of these

(xxii) Integrated Circuits are the main ingredient of

- a) RISC
- b) CISC
- c) Both
- d) Only Super Computer

(xxiii) Which is not a memory slot?

- a) SIMM
- b) DIMM
- c) RIMM
- d) KIMM

(xxiv) What is the full form of UEFI

- a) Uniform Enhanced Firewall Internet
- b) Uniform Extensible Firmware Interface
- c) Uniform Enhanced Firmware Interconnect
- d) None of these

(xxv) Which chip can be reprogrammed to update its contents?

- a) System BIOS
- b) ROM
- c) RAM
- d) Flash ROM

(xxvi) What's a method of clearing the CMOS password?

- a) Send it back to the factory
- b) Flash the BIOS
- c) Change the clock to 00/00/0000
- d) Remove a jumper on the motherboard

(xxvii) What is POST?

- a) post operative self test
- b) PC operating system test
- c) power-on self test
- d) personal overall starter test

(xxviii) Which is the costliest one?

- a) SRAM
- b) DRAM

c) RDRAM

d) SDRAM

(xxix) Which has higher packaging density?

a) SRAM

b) DRAM

c) RDRAM

d) SDRAM

(xxx) POST checks all of the following except:

a) Modem

b) Memory

c) CPU

d) Video

(xxxi) Pentium Series is Intel's _____ generation processor

a) First

b) Second

c) Fourth

d) Fifth

(xxxii) Intel 8085 has _____ bit Address bus

a) 8

b) 16

c) 20

d) 24

(xxxiii) Which slot is mainly used to connect a graphics card?

a) Average Good Presentation

b) Accelerated Grant Processes

c) Accelerated Graphic Port

d) None of these

(xxxiv) If a word cannot be found in cache memory, the incident is called a

a) Cache Hit

b) Cache Penalty

c) Cache Miss

d) Both Cache Penalty and Cache Miss

(xxxv) Only memory, whose speed can be equivalent to CPU is

a) Cache Memory

b) Main Memory

c) Registers

d) None of these

(xxxvi) Speed of a CPU is directly dependent on the width of

- a) Address Bus
- b) Data Bus
- c) Control Bus
- d) None of these

(xxxvii) If CPU finds a desired word in cache, that incident is called

- a) Cache Miss
- b) Cache Hit
- c) Cache Penalty
- d) None of these

(xxxviii) Which stage of instruction cycle is only executed by ALU, not CU

- a) Fetch
- b) Decode
- c) Execute
- d) Write Back

(xxxix) Which is not a chipset?

- a) South bridge
- b) North bridge
- c) Front Side Bus
- d) None of these

(xl) ATX motherboard consist of _____ numbers of pins

- a) 20
- b) 16
- c) 40
- d) 24

(xli) In a SMPS, Ground is indicated by _____ colored wire

- a) Red
- b) Green
- c) Black
- d) Blue

(xlii) Which of the following is associated with motherboard technology?

- a) Advanced Technology
- b) Accelerated Technology
- c) Accelerated Technique
- d) None of these

(xliii) Operating System is an example of

- a) System Software
- b) Application Software

c) Utility Software

d) All of these

(xlv) OSI network model consist of _____ number of layers

a) 5

b) 7

c) 6

d) 8

(xlv) 192.168.1.1 is a _____ IP address

a) Class A

b) Class A

c) Class C

d) Class C

(xlv) TCP/IP network model consist of _____ number of layers

a) 5

b) 7

c) 6

d) 8

(xlvii) In a class A IP address, Network ID is determined by

a) First Byte

b) Second Byte

c) Third Byte

d) Fourth Byte

(xlviii) Starting with 110 as first octet indicates which class IP Address?

a) Class A

b) Class B

c) Class C

d) Class D

(xlix) IP addressing and subnetting is done in

a) Physical Layer

b) Network Layer

c) Transport Layer

d) Application Layer

(l) Size of an IPV4 address is

a) 1 Byte

b) 2 Byte

c) 3 Byte

d) 4 Byte

(li) Maximum number of connecting wires are needed in

- a) Ring Topology
- b) Star Topology
- c) Mesh Topology
- d) Bus Topology

(lii) In a building few computers are connected to form a

- a) LAN
- b) MAN
- c) WAN
- d) None of these

(liii) In mesh topology, if number of node is n, then total number of link will be

- a) n
- b) $2n$
- c) $n(n-1)/2$
- d) $n(n+1)/2$

(liv) Which of the following uses simplex communication

- a) Telephone
- b) Television
- c) Radio
- d) Both (Television) and (Radio)

(lv) Size of an IPV6 address is of

- a) 32 bits
- b) 48 bits
- c) 64 bits
- d) 128 bits

(lvi) Which device is used to boost the signal between two wireless access points?

- a) Hub
- b) Router
- c) Switch
- d) Booster

(lvii) The device that provides central connection point for cables

- a) Router
- b) Hub
- c) Gateway
- d) Repeater

(lviii) Which one is referred as LAN port?

- a) USB
- b) RJ45

c) Type-c

d) HDMI

(lix) Which of the following network topology is most advantageous?

a) Bus Topology

b) Ring Topology

c) Star Topology

d) Hybrid Topology

(lx) In CAT5/CAT6 type cable, how many wires are there?

a) 2

b) 6

c) 8

d) 15