

BRAINWARE UNIVERSITY

Term End Examination 2020 - 21

Programme – Diploma in Electrical Engineering Course Name – Electrical Circuit and Network Course Code - DEE301

Semester / Year - Semester III

Time allotted: 75 Minutes

Full Marks: 60

[The figure in the margin indicates full marks. Candidates are required to give their answers in their own words as far as practicable.]

Group-A

| | (Multiple Cho | ice Type Question) | 1 x 60=60 | |
|------------------------------|---------------------|-------------------------------------|-----------|--|
| 4 | • | ice Type Question) | 1 X 00-00 | |
| 1. (Answer any Sixt | y) | | | |
| (i) Kirchhoff's Current law | is based on law o | f conservation of | | |
| a) energy | | b) momentum | | |
| c) mass | | d) charge | | |
| (ii) The current law represe | ents a mathematica | al statement of fact that | | |
| a) voltage cannot accur | nulate at node | b) charge cannot accumulate at node | | |
| c) charge at the node is | infinite | d) none of the mentioned | | |
| (iii) A semiconductor diod | e is an | element. | | |
| a) Bilateral | | b) Unilateral | | |
| c) Active | | d) Passive | | |
| (iv) The circuit in which co | urrent has a comple | ete path to flow is called | d | |
| a) short | | b) open | | |
| c) closed | | d) open loop | | |
| (v) Potential difference in | electrical terminol | ogy is known as? | | |
| a) Voltage | | b) Current | | |
| c) Resistance | | d) Conductance | | |

| (vi) Pick the incorrect statement among the following | lowing |
|--|--|
| a) Inductor is a passive element | b) Current source is an active element |
| c) Resistor is a passive element | d) Voltage source is a passive element |
| (vii) A practical current source can also be repr | resented as |
| a) a resistance in parallel with an ideal voltage source | b) a resistance in parallel with an ideal current source |
| c) a resistance in series with an ideal current source | d) none of the mentioned |
| (viii) Which of the following is true about an ic | leal voltage source? |
| a) zero resistance | b) small emf |
| c) large emf | d) infinite resistance |
| (ix) A dependent source | |
| a) is always a voltage source | b) may be a current source or a voltage source |
| c) is always a current source | d) none of the mentioned |
| (x) Consider a circuit with two unequal resistar | nces in parallel, then |
| a) large current flows in large resistor | b) current is same in both |
| c) potential difference across each is same | d) smaller resistance has smaller conductance |
| (xi) Ohm's law is not applicable to | |
| a) dc circuits | b) high currents |
| c) small resistors | d) semi-conductors |
| (xii) Conductance is expressed in terms of | |
| a) mho | b) mho/m |
| c) ohm/m | d) m/ohm |

| (xiii) In nodal analysis how many nodes are | taken as reference nodes? | |
|--|------------------------------------|--|
| a) 1 | b) 2 | |
| c) 3 | d) 4 | |
| (xiv) the resonant frequency, the | e current in the inductor lags the | |
| voltage in a series RLC circuit. | 1) D 1 | |
| a) Above | b) Below | |
| c) Equal to | d) Depends on the circuit | |
| (xv) In a series RLC circuit, the phase diffe circuit and the voltage across the capacitor | | |
| a) 0° | b) 90° | |
| c) 180° | d) 360° | |
| (xvi) In a parallel circuit, we consider admi- | ttance instead of | |
| a) Resistance | b) Inductance | |
| c) Capacitance | d) Impedance | |
| (xvii) As the impedance increases, the adm | nittance | |
| a) Increases | b) Decreases | |
| c) Remain Sme | d) Zero | |
| (xviii) In a series R-L circuit, VLVR by | degrees. | |
| a) lags,45 | b) lags,90 | |
| c) leads,90 | d) leads,45 | |
| (xix) The voltage applied across an R-L circ | cuit is equal to of VR and VL | |
| a) arithmetic sum | b) algebraic sum | |
| c) phasor sum | d) sum of the squares | |
| (xx) In a parallel circuit, we consider | instead of impedance | |

| a) Inductan | ce | b) Admittance |
|-------------------------------------|---|----------------------------------|
| c) Resistano | ce | d) Capacitance |
| (xxi) Which, ar | nong the following is the correc | t expression for admittance? |
| a) $Y = Z$ | | b) Y=1/Z |
| c) Y=Z2 | | d) Y=1/Z2 |
| (xxii) What is t and the Q facto | he voltage across the capacitor vor is 10? | when the source voltage is 100V |
| a) 100V | | b) 10V |
| c) 1000V | | d) 0V |
| (xxiii) In select bandwidth freq | ive circuits, the resonant frequence | ncy lies in the of the |
| a) Beginnin | g | b) End |
| c) Midpoint | t | d) Cannot be determined |
| | C series resonant circuit magnitu changing the value of | de of resonance frequency can |
| a) R only | | b) L only |
| c) C only | | d) L or C |
| (xxv) For a vol should be | tage source to be neglected, the | terminals across the source |
| a) replaced | by inductor | b) short circuited |
| c) replaced | by some resistance | d) open circuited |
| (xxvi) In Super sources are? | position theorem, while conside | ring a source, all other current |
| a) open circ | euited | b) short circuited |
| c) change it | s position | d) removed from the circuit |
| | | |

| (xxvii) For the Reciprocity Theorem to satisfy excitation before and after the source is replace | • | | |
|--|---|--|--|
| a) different | b) before source is replaced is greater than after the source is replaced | | |
| c) same | d) before source is replaced is less than after the source is replaced | | |
| (xxviii) While considering Reciprocity theorem to excitation as ratio of? | n, we consider ratio of response | | |
| a) voltage to voltage | b) current to current | | |
| c) voltage to current | d) None of the above | | |
| (xxix) The maximum power is delivered from a source to its load when the load resistance is the source resistance. | | | |
| a) greater than | b) less than | | |
| c) equal to | d) less than or equal to | | |
| (xxx) If there are N nodes in a circuit, then the can be formed are? | number of nodal equations that | | |
| a) N+1 | b) N | | |
| c) N-1 | d) N-2 | | |
| (xxxi) Norton's current is equal to the current passing through the circuited terminals. | | | |
| a) open, output | b) short, input | | |
| c) open, input | d) short, output | | |
| (xxxii) The condition for maximum power to be transferred to the load is? | | | |
| a) Source resistance greater than load resistance | b) Source resistance equal to load resistance | | |
| c) Source resistance less than load resistance | d) Source resistance greater than or equal to load resistance | | |

| (xxxiii) If ZS= RS+jXS, ZL=RL+jXL, then maximum power to be transferred is? | if RL is fixed, the condition for | |
|---|---|--|
| a) XS=XL | b) $XS+XL=0$ | |
| c) XS=-XL | d) None of these | |
| (xxxiv) At resonant frequency, the voltage a voltage across inductor. | cross capacitor is the | |
| a) greater than | b) less than | |
| c) equal to | d) greater than or equal to | |
| (xxxv) While Thevenizing a circuit between | two terminals, V th is equal to | |
| a) Open-circuit terminal voltage | b) Short-circuit terminal voltage | |
| c) Net voltage available in the circuit | d) E.M.F. of the battery nearest to the terminals | |
| (xxxvi) Circuit temporary response that will | die out with time is known as | |
| a) Transient response | b) Steady state response | |
| c) Step response | d) Complete response | |
| (xxxvii) A positive sign on susceptance indic | cates a | |
| a) Capacitive susceptance | b) Inductive susceptance | |
| c) Neutral susceptance | d) resistive susceptance | |
| (xxxviii) The time constant at an R-C circuit | t is? | |
| a) R | b) C | |
| c) RC | d) R/C | |
| (xxxix) If the roots of an equation are real ar be | nd unequal, then the response will | |
| a) Critically damped | b) Under damped | |
| c) Over damped d) Damped | | |

| (xl) It the roots of an equation are real | and equal, than the response will be? | | |
|--|---|--|--|
| a) Critically damped | b) Under damped | | |
| c) Over damped | d) Damped | | |
| (xli) In a loss-free RLC circuit the tran | sient current is | | |
| a) Oscillating | b) Square wave | | |
| c) Sinusoidal wave | d) Non-oscillating | | |
| (xlii) The transient current are associat | ed with the | | |
| a) Impedance of the circuit | b) Applied voltage to the circuit | | |
| c) Resistance of the circuit | d) Charges in stored energy in the inductor and capacitor | | |
| (xliii) The current in the R-L circuit at | a time $t = 0 + is$? | | |
| a) V/R | b) R/V | | |
| c) V | d) R | | |
| (xliv) In an R-C circuit, when the swite | ch is closed, the response | | |
| a) do not vary with time | b) decays with time | | |
| c) rises with time | d) first increases and then decreases | | |
| (xlv) The expression of current in R- C | C circuit is? | | |
| a) $i=(V/R)\exp(t/RC)$ | b) $i=(V/R)\exp(-t/RC)$ | | |
| c) $i=(V/R)-\exp(t/RC)$ | d) $i=(V/R)-\exp(-t/RC)$ | | |
| (xlvi) The steady state part in the expre | ession of current in the R-L circuit is? | | |
| a) $(V/R)(\exp((R/L)t))$ | b) $(V/R)(-exp((R/L)t))$ | | |
| c) V/R | d) R/V | | |
| (xlvii) If the roots of an equation are re | eal and unequal, then the response will | | |

be?

| a) critically damped | b) under damped |
|---|---|
| c) over damped | d) damped |
| (xlviii) If the roots of an equation are complex will be? | conjugate, then the response |
| a) over damped | b) critically damped |
| c) damped | d) under damped |
| (xlix) A series RLC circuit draws current at lead | ding power factor at |
| a) Less than resonant frequency | b) More than resonant frequency |
| c) Resonant frequency | d) Never. |
| (l) A CR network is one which consists of | |
| a) A network consisting of a capacitor only | b) A capacitor and resistor connected in series |
| c) A network consisting of a resistor only | d) A capacitor and resistor connected in parallel |
| (li) The dynamic impedance of a R-L-C paralle | l circuit at resonance is |
| a) R/LC | b) C/LR |
| c) LC/R | d) L/CR |
| (lii) Power in a 3-phase circuit | |
| a) P= 3 VpnIpncos? | b) ?3 VL IL cos? |
| c) P= 3 VpnIpncos? & ?3 VL IL cos? | d) None of these |
| (liii) For a 3-phase, delta connection | |
| a) line current is+B127:B133 equal to phase current | e b) Line voltage is equal to phase voltage |
| c) Line voltage and line current is zero | d) None of these |
| | |

| (liv) Ina 3-phase AC circuit, the sum of all thi | ree generated voltage is | |
|---|---------------------------------------|--|
| a) Infinity | b) One | |
| c) Zero | d) None | |
| (lv) Each coil in three phase alternator has terminals. | number of | |
| a) 8 | b) 6 | |
| c) 4 | d) 2 | |
| (lvi) In wye or star connection,together within the alternator. | of the three phases are joined | |
| a) similar ends | b) opposite ends | |
| c) one opposite end, two opposite ends | d) one similar end, two opposite ends | |
| (lvii) The relation between VRY, Vph in a sta | ar connected system is? | |
| a) VRY =Vph | b) $VRY = ?3Vph$ | |
| c) $VRY = 3.3Vph$ | d) $VRY = 3Vph$ | |
| (lviii) In a delta connected system, the expres | ssion of power (P) is? | |
| a) VLILcos? W | b) 3?3VLILcos? W | |
| c) ?3 VLILcos? W | d) 3VLILcos? W | |
| (lix) Form factor for a sine wave is | | |
| a) 1.414 | b) 0.707 | |
| c) 0.637 | d) 1.11 | |
| (lx) If the resistors of star connected system a impedance ZBY in delta connected system with | | |
| a) (ZRZY+ ZYZB+ ZBZR)/ZR | b) (ZRZY+ ZYZB+ ZBZR)/ZY | |
| c) (ZRZY+ ZYZB+ ZBZR)/ZB | d) (ZRZY+ ZYZB+ ZBZR)/(ZB+ZY) | |