

- c) port C Upper
d) port C Lower
- (ix) In 8085 microprocessor, name the 16 bit register is called
a) Stack Pointer
b) Accumulator
c) Both a and b
d) None of these
- (x) Which one of the following is non-maskable interrupt of 8085 microprocessor?
a) RST 5.5
b) RST 6.5
c) TRAP
d) RST 5
- (xi) Name of typical dedicated register is:
a) PC
b) IR
c) SP
d) All of these
- (xii) Which of the following instructions is an arithmetic instruction?
a) DIV AB
b) ROR
c) STI
d) WAIT
- (xiii) How many register banks are present in 8051 microcontroller?
a) 4
b) 3
c) 2
d) 1
- (xiv) An 8-bit microprocessor has an
a) 8 bit data bus
b) 8-bit address bus
c) 8-bit control bus
d) 8-bit interrupt lines
- (xv) Temporary registers in 8085 are
a) B and C
b) D and E
c) H and L
d) W and Z

Group-B

(Short Answer Type Questions)

3 x 5=15

2. In microprocessor 8085, draw the timing diagram for the instruction of MVI A,45H (3)
3. List the flags of the 8086 microprocessor. (3)
4. Discuss the salient features of the 8051 family of controllers. (3)
5. Explain the operation of the following instruction: (i) JZ addr, (ii) DAA, (iii) RAR, (iv) ADI, (3)
data
6. Write a program logical AND operation of two 8-bit number using 8085 (3)
microprocessor.

OR

Write a program logical OR operation of two 8-bit number using 8085 microprocessor. (3)

Group-C

(Long Answer Type Questions)

5 x 6=30

7. Distinguish between microprocessor and microcontroller. (5)

8. Write down an assembly language program to block of data transfer one memory location to another memory location. (5)
9. With example explain various addressing modes of 8085 microprocessor. (5)
10. Distinguish between 8085 and 8086 microprocessor (5)
11. Explain memory mapped I/O and I/O mapped I/O. (5)
12. Draw and explain the timing diagram of the instruction LXI D, 56FEH (Opcode-3EH) which is stored in the memory location 2004H. (5)

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OR

Explain the meaning of CMP and SUB instruction of 8085 microprocessor. What is the difference between these two instructions? (5)
