



BRAINWARE UNIVERSITY

LIBRARY
Brainware University
Berasat, Kolkata - 700125

Term End Examination 2023
Programme – B.Tech.(ECE)-2019/B.Tech.(ECE)-2020
Course Name – Computer Architecture
Course Code - PCC-EC502
(Semester V)

Full Marks : 60

Time : 2:30 Hours

[The figure in the margin indicates full marks. Candidates are required to give their answers in their own words as far as practicable.]

Group-A

(Multiple Choice Type Question)

1 x 15=15

1. Choose the correct alternative from the following :

- (i) Tell Zero address instruction format is used for
- | | |
|-----------------------------|---------------------------------|
| a) RISC architecture | b) CISC architecture |
| c) Von-Neumann architecture | d) Stack organized architecture |
- (ii) Select that CALL 2030H is an example of
- | | |
|---------------------------|--------------------------------|
| a) Arithmetic instruction | b) Logical instruction |
| c) Branching instruction | d) Machine Control instruction |
- (iii) Identify PUSH and POP is associated with
- | | |
|----------------|----------|
| a) Stack | b) Queue |
| c) Linked list | d) Array |
- (iv) Select which of the following is not a mandatory requirement of a digital computer
- | | |
|-----------------|----------------|
| a) ALU | b) Main Memory |
| c) Cache Memory | d) CU |
- (v) Select in which category Operating System falls
- | | |
|--------------------|-------------------------|
| a) System software | b) Application software |
| c) Compiler | d) None of these |
- (vi) Apply signed-magnitude binary division and find the result if the dividend is (11100)₂ and divisor is (10011)₂
- | | |
|----------|---------|
| a) 10100 | b) 100 |
| c) 11001 | d) 1100 |
- (vii) Choose which one is faster than its counterpart
- | | |
|---------------------------|--|
| a) Carry Propagate Adder | b) Carry Save Adder |
| c) Carry Look Ahead Adder | d) Both (Carry Propagate Adder) and (Carry Save Adder) |
- (viii) Select what an address of main memory is called
- | | |
|--------------------|---------------------|
| a) Logical address | b) Physical Address |
| c) Virtual Address | d) None of these |
- (ix) Calculate if a multiplexer has 16 input lines, then how many select lines it will have?

