





BRAINWARE UNIVERSITY

Term End Examination 2023

Programme – B.Sc.(BT)-Hons-2018/B.Sc.(BT)-Hons-2019/B.Sc.(BT)-Hons-2020

Course Name – Computer Application

Course Code - BBT503C1/BBTD501C

(Semester V)

Full Marks : 60 Time : 2:30 Hours
[The figure in the margin indicates full marks. Candidates are required to give their answers in their

own words as far as practicable.]

Group-A

(Multiple Choice Type Question)

1 x 15=15

- (i) Which of the following allows simultaneous write and read operations?

 a) ROM
 b) EPROM
 c) RAM
 d) None of these
 (ii) Which of the following building block can be used to implement any combinational
- (ii) Which of the following building block can be used to implement any combinational logic circuit?
 - a) AND

b) OR

c) NAND

- d) None of the above
- (iii) Which of the following computer memory is fastest?

Choose the correct alternative from the following:

a) Register

b) hard disk

c) ram

- d) none of these
- (iv) Which of the following is an essential data transfer technique?
 - a) MMA

b) DMA

c) CAD

- d) CAM
- (v) Subtraction in computers is carried out by
 - a) 1's complement

b) 2's complement

C) 3'scomplement

- d) 9's complement
- (vi) Which of the following is the most widely used alphanumeric code for computer input and output?
 - a) GREY

b) ASCII

c) PARITY

- d) EBCDIC
- (vii) The BCD number for decimal 347 is
 - a) 1100 1011 1000

b) 0011 0100 0111

c) 0011 0100 0001

- d) 1100 1011 0110
- (viii) Which of the following return-type cannot be used for a function in C?
 - a) char *

b) struct

c) void

d) none of these

(ix) Who is the father of C language?		
a) Steve Jobs	b) James Gosling	
c) Dennis Ritchie	d) Rasmus Lerdorf	
(x) Why is a pull-up resistor needed for an open	collector gate?	
a) to provide Vcc for the IC	b) to provide ground for the IC	
c) to provide the HIGH voltage	d) to provide the LOW voltage	
(xi) The Program Counter is also called as-	of to provide the to it follows	
a) �� Instruction Pointer	b) DATA COUNTER	
c) Memory counter	d) None of the above	
(xii) register Connected to the Processor b		
	The state of the s	
a) PC c) TEMP	b) IR	
(xiii) When does the output of a NOR gate = 0?	d) Z	
14 Alexander de la companya del companya del companya de la compan	Water and	
a) Whenever a 0 is present at an input b) Whenever a 1 is present at an input	b) Only when all inputs = 0	
(xiv) are used to overcome the difference	d) Only when all inputs = 1	
devices.	in data transfer speeds of various	
	THE AREA TO LOW LOW	
a) Speed enhancing circuitory	b) Bridge circuits	
c) Multiple Buses (xv) ANSI stands for	d) Buffer registers	
	The second secon	-
a) American National Standards Institute b) American National Stand c) American Network Standard Interfacing d) American Network Secur		
c) American Network Standard Interfacing	d) American Network Security Interre	upt
6		
	oup-B Type Questions)	N. C. Park
(Silor Allswer	Type Questions)	3 x 5=15
2. How to generate Gray code?		721
Addition using 1's complement, 1101 and -100	1	(3)
4. What are the valid places where the programm		
5. Explain what is the specialty of NAND and NOR		(3)
6. The difference between accumulator-based and register-based CPU architecture?		(3)
	OR	197
How many bits the accumulator based process	or contains?	(3)
		2.7
Gro	oup-C	
	Type Questions)	5 x 6=30
And the control	Marie Control of the	
7. Discuss about Memory Units.		(5)
8. Draw the block diagram and explain how BCD	to 7- Segment decoder is used to interf	
seven segment display.		
9. Write a program to check Armstrong number	in C?	(5)
10. Write a program to check prime number in C Programming?		(5)
11. Multiply the two numbers 7 and 3 by using the Booth's multiplication algorithm.		(5)
12. Difference between internet & intranet		(5)
The state of the s	OR	
What is Operating System and its application		(5)