



BRAINWARE UNIVERSITY

Term End Examination 2023
Programme – Dip.EE-2019/Dip.EE-2021
Course Name – Applied and Digital Electronics
Course Code - DEE404
(Semester IV)

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Full Marks : 60

Time : 2:30 Hours

[The figure in the margin indicates full marks. Candidates are required to give their answers in their own words as far as practicable.]

Group-A

(Multiple Choice Type Question)

1 x 15=15

1. Choose the correct alternative from the following :

- (i) If the decimal number is a fraction then its binary conversion is obtained by _____ the number continuously by 2.
- a) Dividing
b) Multiplying
c) Adding
d) Subtracting
- (ii) 2's complement of 11001011 is written _____
- a) 1010111
b) 11010100
c) 110101
d) 11100010
- (iii) Binary coded decimal is a collection of _____
- a) Two binary digits
b) Three binary digits
c) Four binary digits
d) Five binary digits
- (iv) The decimal number 10 is represented in its BCD form as _____
- a) 10100000
b) 1010111
c) 10000
d) 101011
- (v) In Digital Circuits, identify which of the following options represent the synchronous control inputs in a T flip flop?
- a) T
b) 0
c) Clock
d) 1
- (vi) Calculate the distance is required for an error correction according to Hamming's analysis?
- a) 1
b) 2
c) 3
d) 4
- (vii) Write how many cycles of addition and shifting in a 4 – bit multiplier are required to perform multiplication using the shift method?
- a) 1
b) 2
c) 4
d) 8

Design a half subtractor using basic gates.

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