



BRAINWARE UNIVERSITY

Term End Examination 2023-2024 Programme – B.Tech.(EE)]-2021 Course Name - Microprocessor & Microcontroller **Course Code - PCC-EE602** (Semester VI)

Full Marks: 60 [The figure in the margin indicates full mar	Thus. Candidates are required to give their ands as far as practicable.]	ime : 2:30 Hour nswers in their
(Multiple of 1. Choose the correct alternative from the f	Group-A Choice Type Question) following:	1 x 15=15
(i) The microprocessor of a computer can o	operate on any information if it is observe	e in
a) Program Counterc) Main Memory(ii) Which of the following technologies was microprocessor?	b) Flag d) Secondary Memory s used by Intel to develop its first 8-bit	
a) NMOS c) PMOS (iii) Select, what is the word length of an 8-b	b) HMOS d) TTL pit microprocessor?	
 a) 8-bits – 64 bits c) 8-bits – 16 bits (iv) Write, In 8-bit microprocessor, how man 	b) 4-bits – 32 bits d) 8-bits – 32 bits ny opcodes are present?	
a) 246c) 250(v) Write, which of the following is not a pro	b) 278 d) 256 operty of TRAP interrupt in microproces	ssor?
a) It is a non-maskable interruptc) It uses edge-triggered signal(vi) Write, which of the following flag is used	b) It is of highest priorityd) It is a vectored interrupt	
a) zero flagc) interrupt flag(vii) Select, how many flip-flops are there in a	b) auxiliary carry flag flag d) sign flag a flag register of 8085 microprocessor?	?
a) 4c) 7(viii) Write, whichhich of the following is not a	b) 5 d) 10 a status flag in microprocessor?	
a) Overflow flag c) Interrupt flag	b) Direction flag d) Index flag	

d) Index flag

write, which of the following is not a condition flag?	
a) Trap flag b) Auxiliary carry fla	g
c) Parity flag d) Zero flag	
(x) A memory connected to a microprocessor has 20 address lines and 1 the memory capacity?	6 data lines. Select
a) 8 KB b) 2 MB	
c) 16 MB d) 64 KB	
(xi) Write, what is the word length of the Pentium-II microprocessor?	
a) 8-bit b) 32-bit c) 64-bit	
d) 16-bit (xii) The instruction, MOV AX, [2500H] is an example of	
a) immediate and the control of the	
a) immediate addressing mode b) direct addressing c) indirect addressing mode d) register addressing	
(xiii) If the data is present in a register and it is referred using the particular	g mode
state	r register, then it is
a) direct addressing mode b) register addressing	ug mode
c) indexed addressing mode d) immediate addressing mode	
(xiv) If the offset of the operand is stored in one of the index registers, the	n it is tell
0\ haaad:l	lexed addressing mode
c) indexed addressing mode d) none of the ment	ioned
(xv) If the location to which the control is to be transferred lies in a differe	ent segment other
than the current one, then the mode is tell	3
a) intrasegment mode b) intersegment dire	ect mode
	ect and indirect mode
	and man cot mode
Group-B	
(Short Answer Type Questions)	3 x 5=15
2. Describe the program counter. How is it useful in program execution?	(3)
3. Name the main registers in the 8086 microprocessor?	
4. List the measures that can be taken to enhance the reliability of the 8086 Microprocessor	
system's bus structure?	36 Microprocessor (3)
5. Write, are the features of Intel 8086?	(3)
6. Explain the purpose of the AX register in the 8086 microprocessor.	
OR	(3)
Explain the function of the Carry Flag (CF) in the 8086 microprocessor.	(2)
	(3)
Group-C	
(Long Answer Type Questions)	5 x 6=30
7. Explain the 8086 Architecture.	(5)
3. Write synchronous data transfer scheme?	(5) (5)
Describe interfacing in the context of microcontrollers?	
O. State, serial interfacing and why is it widely used?	
1. Summarize advanced interfacing techniques used in microcontroller applications?	
2. Write a short note on INTEL 8255?	y- 1
OR	(5)
Express, how DMA is initiated?	
	(5)

7. 8. 9. 10. 11. 12.