



BRAINWARE UNIVERSITY

Term End Examination 2025-2026

Programme – Dip.CSE-2022/Dip.CSE-2023

Course Name – Microprocessor

Course Code - DCSE-PC602

(Semester VI)

Full Marks : 60

Time : 2:30 Hours

[The figure in the margin indicates full marks. Candidates are required to give their answers in their own words as far as practicable.]

Group-A

(Multiple Choice Type Question)

1 x 15=15

1. Choose the correct alternative from the following :

- (i) Identify the pin used to demultiplex the address and data bus in the 8085 microprocessor.
 - a) TRAP
 - b) IO/M'
 - c) ALE
 - d) READY
- (ii) Choose the impact of the 8085 microprocessor's interrupt control system.
 - a) It handles priority interrupt signals
 - b) It executes arithmetic operations
 - c) It stores data temporarily
 - d) It facilitates memory access
- (iii) Choose the correct function of the address buffer in the Intel-8085 microprocessor
 - a) It stores the data to be processed
 - b) It transfers the address of the memory location
 - c) It generates control signals
 - d) It stores the result of the computation
- (iv) Select the correct sequence of stages in the instruction cycle
 - a) Fetch, Decode, Execute, Store
 - b) Decode, Fetch, Execute, Store
 - c) Fetch, Execute, Decode, Store
 - d) Fetch, Decode, Store, Execute
- (v) Select the stages of the machine cycle:
 - a) Fetch, Execute
 - b) Fetch, Decode, Execute
 - c) Fetch, Decode, Store
 - d) Execute, Write
- (vi) Select the comparison between the read cycle and the write cycle:
 - a) Read cycle retrieves data; write cycle stores data
 - b) Both cycles perform the same task
 - c) Write cycle retrieves data; read cycle stores data
 - d) Neither cycle involves memory
- (vii) Select the name of the addressing mode that does the instruction, MOV AX, BX represent?
 - a) register indirect addressing mode
 - b) direct addressing mode
 - c) register addressing mode
 - d) register relative addressing mode

12. Subdivide the various methods of DMA into categories based on their speed and CPU involvement. (5)

OR

- Compare the efficiency of DMA and traditional I/O methods in a microprocessor-based system. (5)

Brainware University
398, Ramkrishnapur Road, Barasat
Kolkata, West Bengal-700125
