



## BRAINWARE UNIVERSITY

## Term End Examination 2023 Programme – BCA-2019/BCA-2020/BCA-2021 Course Name – Computer Organization and Architecture Course Code - BCAC202 (Semester II)

Full Marks: 60

Time: 2:30 Hours

[The figure in the margin indicates full marks. Candidates are required to give their answers in their own words as far as practicable.]

## Group-A

(Multiple Choice Type Question)

1 x 15=15

- Choose the correct alternative from the following :
- (i) Identify the process of fetching and executing instructions, one at a time, in order of increasing address is called
  - a) Instruction execution

b) Straight line sequencing

c) Instruction fetch

- d) Instruction Cycle
- (ii) Select the correct option, The ALU of a computer normally contains a number of high speed storage elements called
  - a) Semi-conductor memory

b) Registers

c) Hard disk

- d) IC
- (iii) Instruction cycle is described as
  - a) fetch-decode-execution

b) fetch-execution-decode

c) decode-fetch-execution

- d) None of these.
- (iv) Judge, the number of instructions needed to add \'n\' numbers and store the result in memory using only one address instructions is
  - a) n

b) n-1

c) n+1

d) independent of n

- (v) A stack is described as
  - a) 32-bit register in the microprocessor
- b) 16-bit register in the microprocessor
- c) set of memory locations in R/W memory reserved for storing information temporarily during the execution of a program
- d)
  16-bit memory address stored in the h
  program counter
- (vi) Identify the unit of a computer system which executes program, communicates with and often controls the operation of other subsystems of the computer is the
  - a) CPU

b) Control unit

c) both CPU & Control unit

d) Peripheral unit

e cache
_ bits.
mount
mount
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vith
3 x 5=15
dictor. (3)
(3)
(3)
(3)
(3)
(3)
5 x 6=30
(5)
(5)
(5)
(5)
(5)

12. Define the purpose of a pipeline register in a pipelined processor?	(5)
OR	
Describe the difference between a hard and soft disk?	(5)
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