

BRAINWARE UNIVERSITY

Term End Examination 2021 - 22

Programme - Bachelor of Technology in Electronics & Communication Engineering **Course Name – Analog Circuits Course Code - PCC-EC402** (Semester IV)

Time allotted: 1 Hrs.15 Min. Full Marks: 60

[The figure in the margin indicates full marks.]

Group-A

60=60

Choose the correct alternative from the	(Multiple Choice Type Question) he following:	1 x 6
(1) Which of the following is not true in the	he circuit for digital-to- analog voltage converter?	
a) Some logic circuitry	b) A resistor network	
c) A reference voltage	d) A resonant circuit	
(2) Which one of the following D/A conv	erters has the resolution of approximately 0.4% of its ful	ll scale range?
a) 8-bit	b) 10-bit	_
c) 12bit	d) 16bit	
(3) Cascade amplifiers are used for		
a) Voltage amplification	b) Video amplifiers	
c) Power amplifier	d) Tuned amplifier	
(4) Stability can be improved in an op-am	p by	
a) Pole zero compensation	b) Dominant pole compensation	
c) Leads compensation	d) All of these	
(5) The voltage follower is commonly use	ed as	
a) Switch	b) Isolator	
c) Regulator	d) None of these	
(6) Which of the following are the non-lin	near applications of OP-AMP?	
a) Current-to-voltage converter	b) Comparator	
c) Peak detector	d) Limiter	
(7) The main function of a voltage regulat	tor is to provide a nearly output voltage.	
a) Sinusoidal	b) Constant	
c) Smooth	d) Fluctuating	
(8) Thermal shutdown occurs in an IC reg	gulator if	
a) Power dissipation is too low	b) Internal temperature is too hig	gh
c) Current through the device is too lov	w d) Any of these occurs	

(9) R-C coupling is proper in low-level AF amplifier because it

a) Is inexpensive and needs no adjustment	b) Has better low frequency response
c) Needs low voltage battery	d) Provides an output signal in phase with input signal
(10) An OPAMP has	
a) Equal input and output resistance	b) Low input resistance and a large output resistance
c) Large input resistance and low output resistance	d) None of these
(11) The open loop voltage gain of an OPAMP is	
a) Small	b) Large
c) Can be anything	d) Unity
(12) Which of the following are true about capacitor filter?	
a) It is also called as capacitor output filter	b) It is electrolytic
c) It is connected in parallel to load	d) It helps in storing the magnetic energy
(13) The inductor is placed in the L section filter because	
a) It offers zero resistance to DC component	b) It offers infinite resistance to DC component
c) It bypasses the DC component	d) It bypasses the AC component
(14) The output waveform of CLC filter is superimposed by	a waveform referred to as
a) Square wave	b) Triangular wave
c) Saw tooth wave	d) Sine wave
(15) A common mode signal is applied to	
a) The non-inverting input	b) The inverting input
c) Top of tail resistor	d) Both inputs
(16) Phase shift oscillator has the following advantage	
a) It does not contain transformer or inductor	b) It provides good frequency stability
c) It can produce low frequency	d) It provides very small feedback
(17) Wein bridge oscillator has the following disadvantage	
a) It can generate frequency up to 1 MHz only	b) It requires large number of components
c) Output is constant	d) There is a loading effect
(18) An Wein bridge oscillator uses	
a) Positive feedback	b) Negative feedback
c) Both types of feedback	d) An LC tank circuit
(19) We use crystal oscillator because	
a) It gives high output voltage	b) It works at high frequency
c) Frequency of oscillation remains substantially constant	d) It requires very low dc supply voltge
(20) Which topology of feedback amplifier has very high inj	put and output impedances?
a) Voltage series feedback	b) Voltage shunt feedback
c) Current series feedback	d) Current shunt feedback
(21) Voltage shunt feedback amplifier is a	
a) Transconductance amplifier	b) Transresistive amplifier
c) Voltage amplifier	d) Current amplifier
(22) For a base current of 10 μA , what is the value $\beta dc = 100$	e of collector current in common emitter if
a) 10 μA	b) 100 μA
c) 1 mA	d) 10 mA
(23)	•

A BJT with forward current transfer ratio α = provides current transfer ratio β as	= 0.98, when working in CE mode,
a) 98 c) 49	b) 0.02 d) 0.49
(24) Which one of the following is correct?	u) 0.49
2)	h)
$h_{fe} = \alpha$	b) $h_{fe} = \beta$
$^{\rm c)}$ ${\rm h_{fe}}$ =- α	d) $h_{fe} = -\beta$
(25) The slew rate for output voltage $V_0 = 2 \sin \alpha$	ot, of an op-amp is
a) _w	b) 2ω
c) 1/w	d) 2/ω
(26) Common-emitter amplifier circuit with emitter feedback, the	e input impedance is nearly equal to
a) h _{fe}	b) R _E
c) h_{fe}/R_{E}	d) $h_{fe}R_{E}$
(27) The voltage gain of single-stage CE amplifier increases with	1
a) Increase in ac load resistance	b) Decrease in ac load resistance
c) Increase in source resistance	d) Increase r _e
(28) The frequency of oscillation of a Hartley oscillator is	
a) $f=1/2\pi(L_{eq}C)^{1/2}$	b) $f=1/2\pi(LC_{eq})^{1/2}$
c) $f=1/2\pi RC\sqrt{10}$	d) $f=1/2\pi RC$
(29) A $100\mu F$ capacitor when used as a filter has 2.5K Ω . If the filter is the full wave and supple percentage of ripple frequency in the output?	y frequency is 50Hz, what is the
a) 2.456%	b) 1.154%
c) 3.785%	d) 3.675%
(30) A single phase full wave rectifier makes use and a choke of 10henry. The secondary volta Determine the dc output voltage when f=501	age is 280V and the load current is 100mA.
a) 345V	b) 521V
c) 243V	d) 346V
(31) Barkhausen Criterion for oscillator stability is	LX
^{a)} $A\beta = 0$	b) $A\beta = \pm 1$
^{c)} $A/\beta = 1$	d) $A = 1/\sqrt{\beta}$
(32) The transfer characteristic of a transconductance amplifier is	S

b) $A_i = I_0/Is$

a) $g_m = I_0/V_S$

c) $Av = V_0/V_s$	d) $r_m = V_0/Is$
(33) A digital-to-analog converter with a full scal	e output voltage of 3.5 V has a resolution close to 14 mV. Its bits size is
a) 4	b) 8
c) 16	d) 32
(34) An 8-bit converter is used for a dc range of (0-10V. Find the weight of LSB.
a) 39mV	b) 78mV
c) 39.2mV	d) 40mV
(35) For a base current of 10 μA, what is the valu	the of collector current in common emitter if $\beta dc = 100$
a) 10 μA	b) 100 μA
c) 1 mA	d) 10 mA
(36) If an amplifier with a gain of -1000 and fee change in the gain of the feedback amplifier	dback factor $\beta = -0.1$ had a gain change of 20% due to temperature, the would be
a) 10%	b) 5%
c) 0.2%	d) 0.01%
(37) The number of comparators required in a 3-b	pit comparator type ADC is
a) 2	b) 3
c) 7	d) 8
(38) A BJT with forward current transfer ratio α	= 0.98, when working in CE mode, provides current transfer ratio β as
a) 98	b) 0.02
c) 49	d) 0.49
(39) The feedback element in the integrator is a	
a) Capacitor	b) Inductor
c) Resistance	d) diode
(40) With zero volt on both inputs, an ideal op-an	np should have an output voltage equal to
a) Positive supply voltage	b) Negative supply voltage
c) Zero	d) CMRR
(41) An operational amplifier has an open-loop g differential voltage of the amplifier is	ain of 200,000. Its output exhibits saturation at 10V. The threshold
a) 25μV	b) 50μV
c) 0.5 mV	d) 10V
(42) An op-amp has a common mode gain of 0.0	l and a differential mode gain of 105. Its CMRR would be
a) 10^-7	b) 10^-3
c) 10^3	d) 10^7
(43) If the differential voltage gain and the comm respectively, then its common mode rejection	
a) 23dB	b) 25dB
c) 46dB	d) 50dB
(44) The output of a certain op-amp circuit chang	ges by 20 V in 4 μs. Its slew rate is
a) 50 V/μs	b) 5 V/μs
c) 500 mV/μs	d) 500 V/μs
(45) The slew rate for output voltage $V0 = 2 \sin \alpha$	ot, of an op-amp is
a) w	b) 2ω
c) 1/w	d) 2/ω
(46) Inverting op-amp is	
a) Voltage shunt feedback	b) Voltage series feedback
c) Current series feedback.	d) Current shunt feedback

(47) An instrumentation amplifier uses	
a) 1 op-amp	b) 2 op-amp
c) 4 op-amp	d) 3 op-amp
(48) The zero level detector is one application of a	
a) Differentiator	b) Integrator
c) Summing amplifier	d) Comparator.
(49) The current gain of a common-base amplifier is	
a) Less than unity	b) Greater than unity
c) Unity	d) Zero
(50) The voltage gain of single-stage CE amplifier depends on	
a) rL/re	b) re/rL
c) rerL	d) None of these
(51) The gain of a BJT drops at high frequencies due to	
a) Coupling and bypass capacitor	b) Inter-electrode transistor capacitances
 c) Coupling and bypass capacitor and inter-electrode transistor capacitances 	d) Early effect
(52) Which transistor bias circuit provides good Q-point stability	y with a single-polarity supply voltage?
a) Base bias	b) Collector-feedback bias
c) Voltage-divider bias	d) None of these
(53) Which of the following oscillators is used for generating lo	w frequencies?
a) RC phase shift oscillator	b) LC oscillator
c) Wien-bridge oscillator	d) Blocking oscillator
(54) Which of the following statements is true of phase-shift typ	be and Wien-bridge-type RC oscillator?
a) Both use positive feedback.	b) The phase-shift type oscillator uses positive feedback only whereas Wien-bridge oscillator uses both positive and negative feedback.
c) The phase-shift-type oscillator uses both positive and negative feedback whereas the Wien-bridge oscillator uses positive feedback only	d) Both use negative feedback
(55) The % load regulation of a power supply providing 100V u	nloaded & 95V at full load is
a) 5.3%	b) 5.0%
c) 0.53%	d) None of the above
(56) Which of the following voltage regulator is preferred for pr	oviding large value of load current?
a) Zener diode shunt regulator	b) Transistor series voltage regulator
c) Transistor shunt voltage regulator	d) None of the above
(57) A transistor series voltage regulator is called emitter follow follows thevoltage.	rer regulator because the emitter of the pass transistor
a) Output	b) Input
c) Base	d) Collector
(58) If the output of voltage regulator varies from 20 to 19.8V w the line regulation is	hen the line voltage varies over its specified range 0.2 V,
a) 0	b) 1%
c) 2%	d) 5%
(59) Compared to the ripple into a voltage regulator, the ripple of	out of a voltage regulator is
a) Equal in value	b) Much larger
c) Much smaller	d) impossible to determine
(60) A shunt regulator is inefficient because	

- a) It wastes power
- c) The ratio of output to input power is low
- b) It uses series resistor & shunt transistor
- d) All of the above